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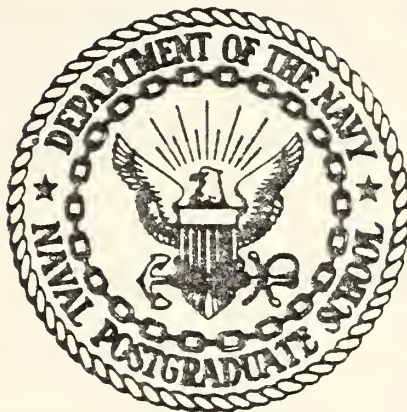
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Monterey, California



THESIS

AN INTERACTIVE COMPUTER INTERFACE
WITH A DIGITAL RECEIVER

by

William Glenn Borries

March 1977

Thesis Advisor:

S. Jauregui

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An Interactive Computer Interface with
a Digital Receiver

by

William Glenn Bonries
Lieutenant, United States Navy
B.S., United States Naval Academy, 1970

Submitted in partial fulfillment of the
requirements for the degree of

MASTER OF SCIENCE IN ELECTRICAL ENGINEERING

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A computer interface to connect both the Applied Technology Airborne Computer (ATAC) and the KIM-1 Microprocessor to a Watkins Johnson digitally tuned receiver was designed and constructed. The existing ATAC computer program was modified.

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LIST OF ABBREVIATIONS

| | |
|-------|---|
| A/D | Analog to Digital |
| ASCII | American Standard Code for Information Interchange |
| Baud | Bits per second |
| D/A | Digital to Analog |
| high | TTL logic 1 (+5v) |
| I/O | Input and/or Output |
| IC | Integrated Circuit |
| IF | Intermediate Frequency |
| ISR | Intermediate Sideband |
| low | TTL logic 0 (0v) |
| LSB | Lower Sideband |
| TTL | Transistor Transistor Logic |
| USR | Upper Sideband |
| RFO | Beat Frequency Oscillator |

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I. INTRODUCTION

For many decades man has dreamed of the day when machines could relieve him of much of his work. In this era of computers and advanced technology, this dream is now becoming a reality. Connecting computers to other machines, however, is not just a simple matter of running a wire from one to the other. In order for the computer to be able to use its "thinking" ability, it must have some way to translate its signals into a form that is recognized by the machine it is controlling. This is where the interface becomes all important.

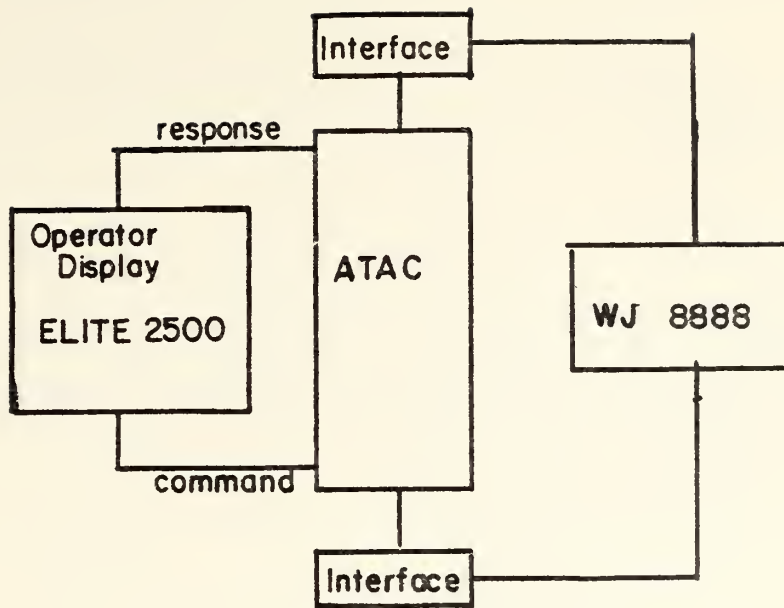
An interface is a piece of equipment placed in the data path between two devices. Its purpose is to rearrange, translate, or change the speed of this data to meet the needs of one or both devices. In other cases the interface is used to convert data from an analog to digital (A/D) or digital to analog (D/A) form, or both. Interfaces of either type range in complexity from a few integrated circuits to the use of microprocessors. Most, however, fall in between. This thesis discusses the design and construction of an interface in this middle class. Here, the computers are the Applied Technology Airborne Computer (ATAC) minicomputer and the MOS Technology Inc.'s 6801 microprocessor. Their goal is to program and process outputs from a digitally tunable Watkins Johnson WJ-8888.

The two computer systems arrive at their goal by

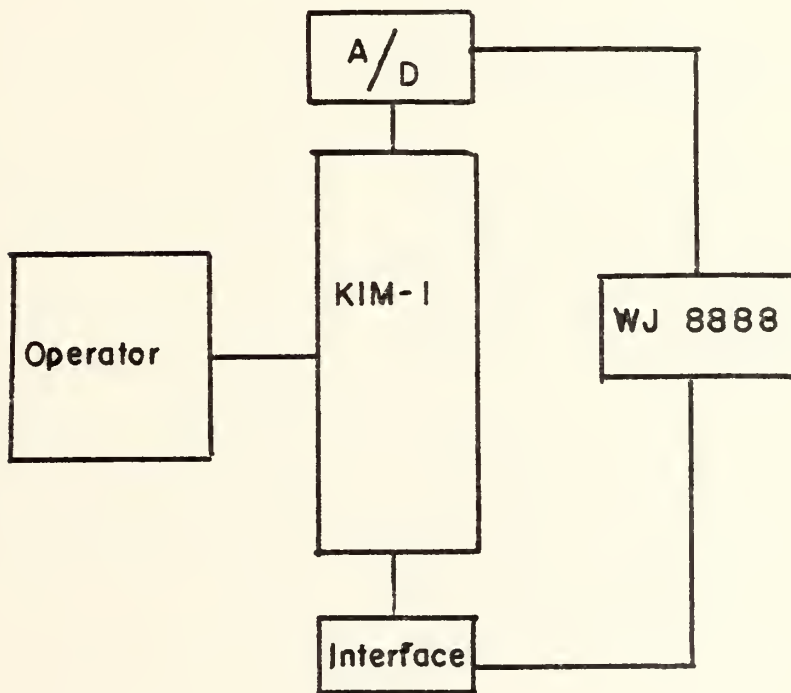
different means. The ATAC uses a closed loop with the operator (Figure 1a) while the KIM-1 excludes the operator while executing its program (Figure 1b). In the ATAC loop the operator actively controls all communication between the computer and receiver. In this way it is possible to display information from the receiver on the video display at any time except during a scan (see Chapter V). It also provides quick reference to the data to be sent, the data last sent, and latest received data. This was invaluable during debugging. From the terminal it is also possible to adjust available parameters as necessary to meet any requirement.

The KIM-1 does not directly exchange digital words with the receiver, but rather exchanges digital data for analog data. This does not provide a feedback loop that includes the operator. Once begun, the KIM-1 program selects and sends data words to the receiver and processes the analog data received until the program comes to an end or is halted by the operator. Direct information is not available to determine when or if a digital word has been sent or received correctly.

Problems encountered during the design and construction of the interface and their solutions are shown in Table I. In this instance signal level compatibility was not a problem because the I/O from the receiver, the interface, and the two computers were all TTL logic levels and, therefore, matched. It is believed that these problems are a typical list that may be encountered when interfacing.



(a)



(b)

Figure 1
Computer Control

PROBLEM

SOLUTION

1. Noise on the ATAC I/O lines.

1. Use of Schottky circuits reduced or eliminated the noise.

2. Different clock rates of the computers and the receiver, and different data word lengths.

2. ATAC; converted parallel outputs into serial form. KIM-1; used interrupt lines to slave the KIM-1 to the receiver's clock.

3. Timing

3. Identified receiver periods by the Monitor Clock output. This provided a pulse which signaled stable data.

4. Inputting data to the ATAC.

4. Open collector buffers were used to sink the required current for proper data transfer.

5. Switching between the ATAC and the KIM-1.

5. Multiplexers and buffers were used to switch between the two computers.

Table I

Problems and Solutions

II. THE RECEIVER

The Watkins Johnson WJ-8888 (WJ) is an HF receiver designed for use in the 550 KHz to 30 MHz band. Its advantages include the ability to detect and output both the AM and FM IF signals while simultaneously maintaining a separate output of eight selectable detection modes. Options available to the operator include different IF bandwidths, variable RF gain, squelch control, and a tuneable BFO frequency. The WJ is digitally controlled and uses a 64-bit word as shown in Figure 2. This word contains the information necessary to transfer the frequency, detection mode, IF bandwidth, RF gain, BFO frequency, and signal strength both internally and externally.

All inputs and outputs from the receiver are controlled by the synchronous, remote I/O board. This board is a gated transfer point for all digital data exchanged with the receiver. A number of control lines are needed to provide the necessary demands on the receiver. Three balanced input pairs and four balanced output line pairs, plus a ground are provided for this purpose. All three inputs are required for remote operation. They are address (or enable), trigger, and data input. The address pair is the most important for it serves as the master "on-off" switch for the remainder of the I/O pairs. The outputs furnish the required clocks (command and monitor), output data, and a local/remote status.

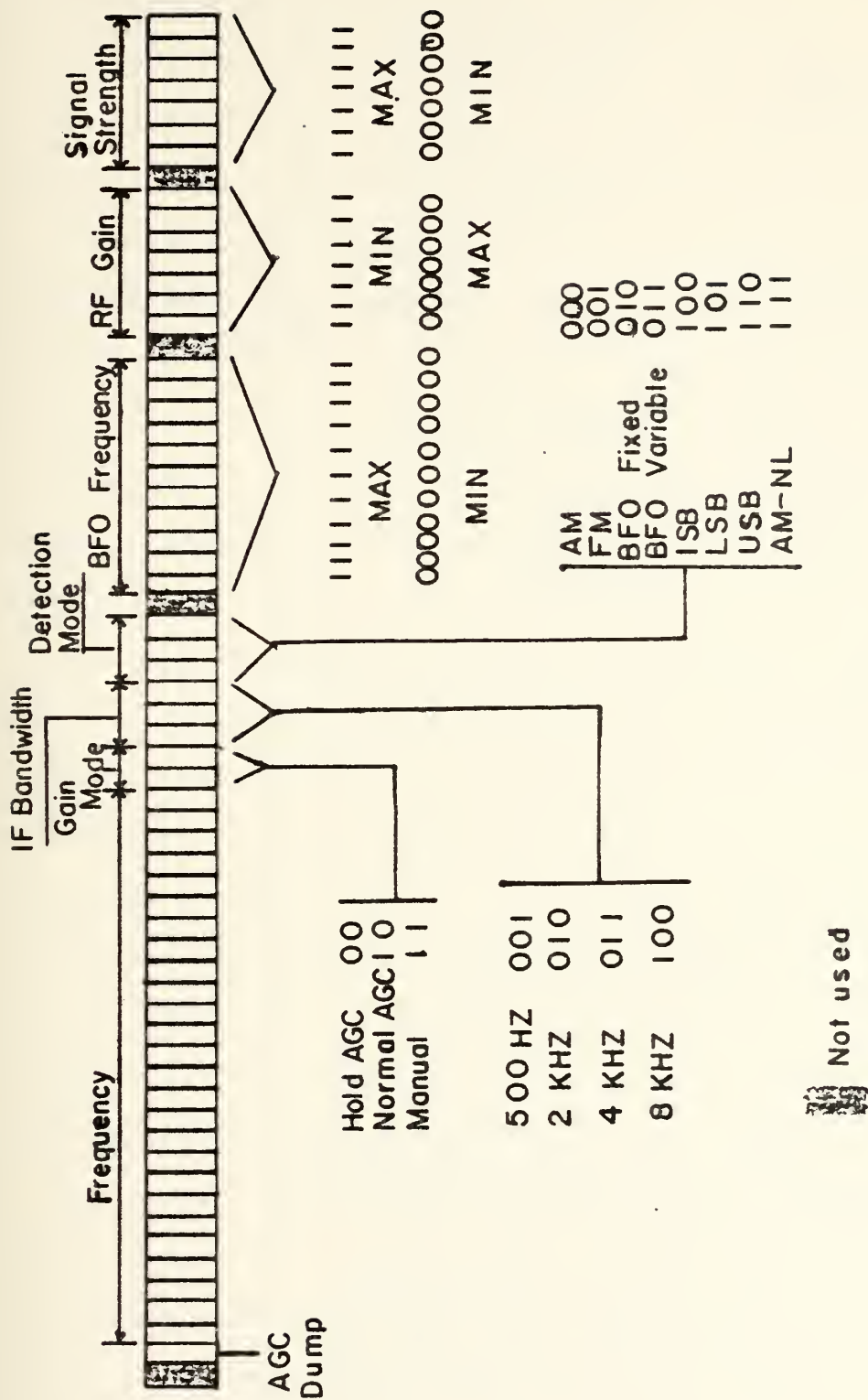
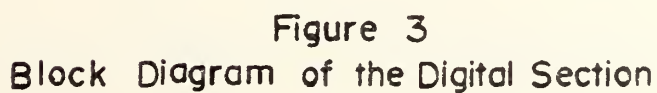


Figure 2
Receiver's Word

The Watkins Johnson operates on a sequential cycle divided into four equal periods and six identifiable modes. The periods regulate the different operations while the modes ascertain the origin of the data. Table II shows the interaction of the periods and modes of the receiver. Three of the six modes are memory read and write functions; these cannot be remotely controlled and, therefore, are of no concern here. Of the remaining three, two are the remote active and remote passive modes. These allow the introduction of externally generated data and prevent manual intervention during all but one of the four periods. Manual control is available in the remaining mode, local.

In order to manage the data word movement correctly, the receiver utilizes a common bus or data node arrangement as shown in Figure 3. This simplifies operation by forcing all data words to pass through this node in the same direction, regardless of their origin or desired destination. The multiplexer controls the input to the data node. Control of the multiplexer and, therefore, the origin of the data is managed by the internal modes of the receiver. The objective of period one is to load the receiver register. In the local and remote passive modes, the data word is shifted from the front panel register, through the multiplexer and data node, into the receiver register. The difference between these two modes is in the action of the data prior to shifting. The local mode updates the data word from the front panel storage registers during the early



| Local | 1 | 2 | 3 | 4 |
|-------------------|---|---|-------------------------------------|--|
| | Load Front Panel Reg from Front Panel/word shifted to Receiver Register | Load Rcvr Storage Key/ Signal Strength and RF gain updated /word shifted to FrontPanel Register | Display updated no words shifted | Change to Remote Active or Remote Passive possible |
| Remote Passive | Word shifted from FrontPanel Register to Receiver | Update only Sig Str /Load Rcvr Storage Reg/word shifted to FrontPanel Reg. | Same as Local | Change to Lcl or Remote Active possible |
| Remote Active | Word shifted from I/O module to Receiver Register | Same as Remote Passive | Same as Local | Change to Remote Passive automatic. Change to Local possible |

Table II

Receiver Modes and Periods

portion of period one. This action is inhibited during the remote passive mode. In the remote active mode the data word originates from a remote device, is shifted by the command clock through the remote I/O board, on to the receiver register via the multiplexer and data node.

The first part of the second period is spent loading the data shifted during period one into the receiver storage registers. During this time the signal strength is updated in the receiver register regardless of the mode. The RF gain A/D-D/A converter functions according to the selected mode. In the local mode the RF gain bits in the data word are replaced by A/D conversion of the front panel RF gain control knob. The two remote modes reverse this action and load the RF gain D/A converter with this data from the word. After this is completed, the word is shifted in all modes out of the receiver register, through the multiplexer and data node, and into the front panel register. If the address line from the remote device is active high, the data word and the monitor clock are available on their respective output line pairs.

Periods three and four inhibit movement of the data word. Period three updates the front panel pushbutton lights and numeric display. Period four is the only period in which changes in receiver mode are allowed. During this period changes from a remote mode to local, or from local directly to remote passive can only be accomplished by depressing the appropriate pushbutton on the front panel. A

change from local and remote passive to remote active is automatically done by the remote I/O board whenever both the address and trigger line pairs are active high during this period. The remote active mode immediately reverts to the remote passive mode at the beginning of the next period four. The total cycle time of the receiver is 10.24 msec (2.56 msec per period). In order to change modes successfully, it may be necessary either to hold in the pushbutton or to hold the trigger and address lines high for up to 7.68 msec (three periods). This ensures that the mode change demand occurs in period four.

All outputs are available from connectors J1, and J6 through J10 located on the back of the receiver. J1 is the digital I/O connector. The other connectors are all analog outputs. J6 is a 455 KHz IF signal of at least 20 KHz bandwidth. AM and FM detector monitors are provided at connectors J7 and J9 respectively. J8 is a predetection, 455 KHz center frequency IF output whose bandwidth is set by the front panel. A balanced and unbalanced line audio and both upper and lower sideband outputs are available from the appropriate pins at J10. The balanced line operates at all times. The unbalanced line is operable unless headphones are plugged into the front panel. The lower sideband output is active when the receiver is in either ISB or LSB detection modes, and the upper sideband output is active during ISB, USB, and CW modes.

III. THE COMPUTERS

After studying the inputs and outputs from the receiver, three choices were available for further development of the interface. It could be designed to pass the clock pulses on to the interrupt lines of the computer and, therefore, match the computer's timing to that of the receiver. Or, a buffer could be constructed to input the data serially at the clock rate of the computer and output it at the clock rate of the receiver. The third choice, also a buffering arrangement, could exchange data in parallel to the computer and serially to the receiver.

The chief factor influencing the design decision was the availability and distribution of computer control and I/O lines. For the first computer, the primary objective was to investigate the feasibility of both remotely tuning the receiver and accepting a data word in return. The requirements for the second computer, the MOS Technology Inc. KIM-1, were less strict. Its objective was to tune the receiver digitally through use of the interface. Its input, however, was to come from a A/D converter for processing.

A. THE ATAC

The ATAC was originally designed to provide EW service to aircraft. Built to do real-time analysis of signals, it has very short cycle times, optional microcode programming, and double precision arithmetic as part of the standard package. All this, combined with its large instruction set, makes the ATAC a versatile and powerful tool. Although data could be transferred serially by proper programming, the ability of the ATAC to both input and output sixteen bits in parallel on the PIO (parallel input/output) lines proved more advantageous. Any one of the ATAC's sixteen registers can input or output from these lines. In order to properly transfer this data, the PIO bus must be augmented by an address provided by the sixteen bits of the "extended" Arithmetic Register (XAR). Another necessary output is one that informs the external device when the ATAC is ready for the transfer. On the ATAC this function is provided by the Input/Output Demand (IOD) line. Referring to the timing diagram in Figure 4, an input command is initiated by placing an address on the XAR lines and following this address with a low on the IOD. This signifies that the ATAC register is ready for data. After approximately one microsecond, the IOD is placed high and the address is removed. During this microsecond the data for the ATAC must be stable. For an output command, the XAR and the PIO lines first present the address and data for output. When they

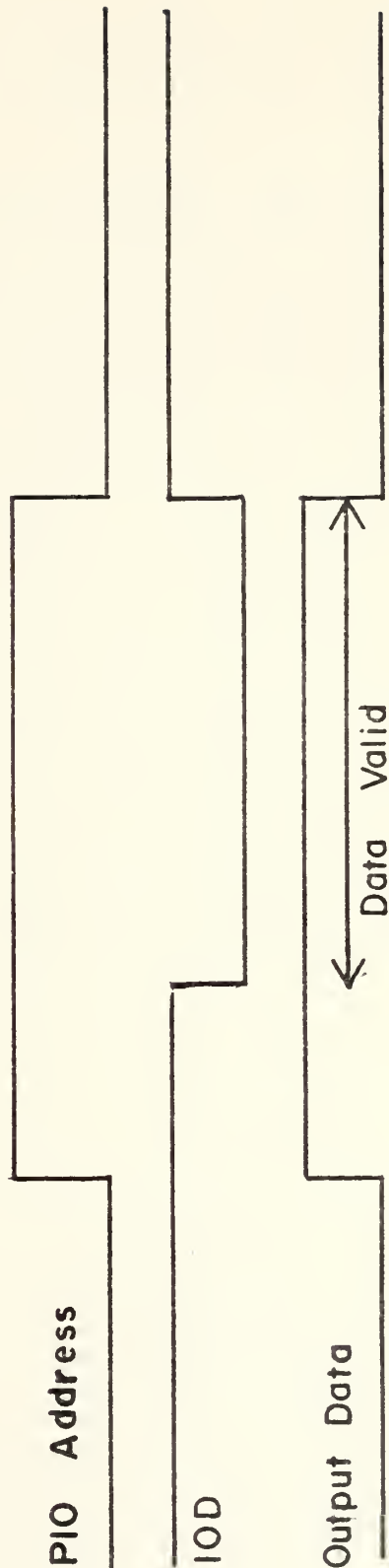
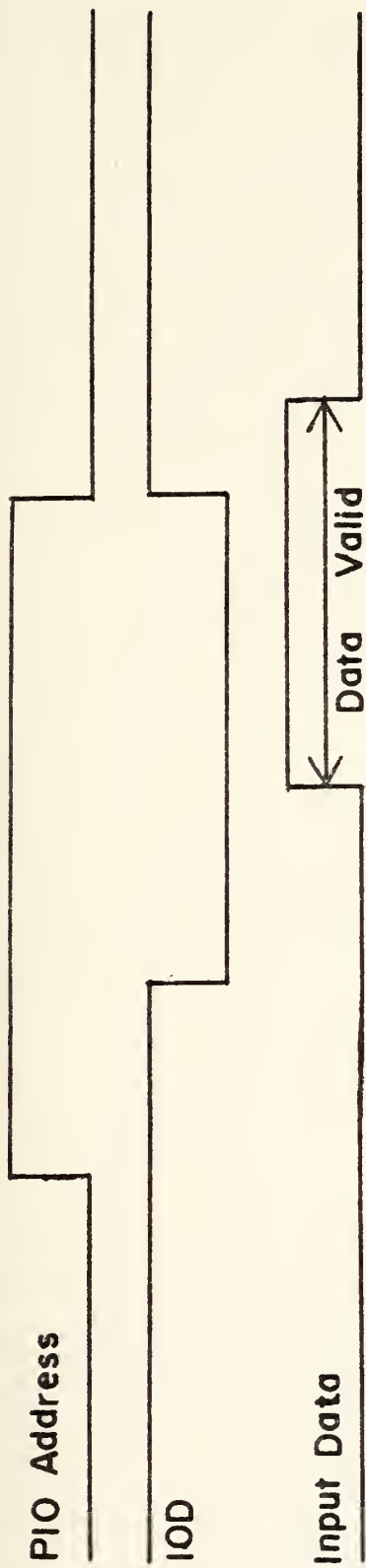


Figure 4
ATAC I/O Timing Diagram

are stable, the IOD line is set low. The data is then available for about a microsecond, as before. The IOD line is then placed high and the address and data are removed from their respective lines.

For operator interaction a serial ASCII, RS232 I/O port is also available. A Datamedia Elite 2500 television terminal is connected here to provide the operator with the necessary control and programming capability for use of the ATAC. By proper programming and use of the XAR lines, it was possible to translate each command for the interface. Using a demultiplexer on the interface board, four of the five available XAR addresses were separated into sixteen separate commands. One of the remaining lines and the IOD line were used as strobes to identify the receiver and to signify stable data (Chapter III). This arrangement provided both the adequate isolation and flexible operation desired.

B. THE KIM-1

The KIM-1 is at the other end of the computer spectrum with respect to the ATAC. It is a microprocessor designed around the MOS Technology Inc. series MCS8500 Central Processor Unit. Complete on a single printed circuit board, the KIM-1 is simple to operate and easy to program. While its cycle time is slower than that of the ATAC, it is still much faster than the receiver and more than adequate to meet

the requirements. Since the input data came from converted analog data supplied from the receiver's FM IF output (J9) and an external A/D converter, the design for this portion of the interface was simpler.¹

¹For a more detailed discussion of the KIM-1, its objectives, programs, and operating procedures, see Signal Acquisition and Sampling Using a Microprocessor, by LT. D. Rosenbender.

IV. THE INTERFACE

The interface was initially designed solely for the ATAC. A means of converting four ATAC words into one receiver word was needed first, in order to test the program, the computer, and the receiver together. The simplest and cheapest way to accomplish this conversion and still fully utilize the capabilities of the ATAC was to build a 64-bit register using eight parallel-in, serial-out, eight-bit shift registers. A control section was also necessary to properly handle this data. The ATAC XAR addresses were decoded by this control section to provide the load commands for the registers and to signal the receiver to input the word.

The next step in construction was also simple in theory. Since the computer uses the PIO lines for input as well as output, what was needed was a connection which would not interfere with the section already built. The ICs chosen to isolate the two sections are called Tri-State. These ICs have a "no output" state in addition to the normal high and low of TTL circuits. They could not, however, sink or supply enough current to drive the computer PIO bus. A solution was found by following these ICs with open collector buffers. Not only did they provide the necessary amplification, they did not degrade the isolation performance of the Tri-States. This second section also had

a 64-bit register built from the smaller shift registers. In this case, though, they were serial-in, parallel-out. In order to remove the word from the register in sixteen-bit sections, the outputs from the shift registers were connected to four-to-one multiplexers. These multiplexers were Tri-State. With the proper control it was possible to shift the word from the receiver into this register, and transfer it to the PIO bus in the correct sequence.

Increased complexity in the control section came with this implementation. A method was needed to prevent the computer from transferring a word until it had been completely shifted into the register. The period two clock output from the receiver was used as a reference to provide a pulse to inform the computer when shifting was complete. This pulse was positioned in the same time interval as period three of the receiver. The additional benefit of identifying period four was obtained. This meant that the output for the tridden line to the receiver could be shorter and still meet the requirement to occur in a portion of period four.

After completion of the testing for the ATAC, an interface was designed and constructed for the KIM-1. This design was very simple to implement, since all the necessary timing circuits were already built and tested. The two computers were kept from interfering with each other by installation of a manual switch. This switch controls the

address of a multiplexer that separates the lines in the interface common to both computers. The control section was wire-wrapped rather than placed on a printed circuit board to provide greater flexibility, easier maintenance, and to reduce cost.

A. THE CONTROL SECTION

The heart of the interface is the control section (Figures 5 and 6). The main purpose of this section is to decode and route commands from the ATAC and provide the necessary circuits to interface with the receiver. It also contains the circuits for the operation of the receiver by the KIM-1. The receiver's outputs are driven by line drivers which provide complementary TTL levels. The inputs are applied to line receivers which accept these complementary TTL levels. The interface, therefore, had to use these same receivers and drivers to be compatible with the Watkins Johnson.

The SPST switch mounted on the front of the interface case selects the computer controlling the receiver. With the switch in the ATAC position, a high is placed on pin 1 of IC-JJ and pins 1 and 10 of IC-MM. IC-JJ is now set up to transfer the following: the address and data outputs to the line drivers on IC-LL, the trigger command to pin 2 of IC-Z, and a low in line CCK7. The CCK7 line completes the

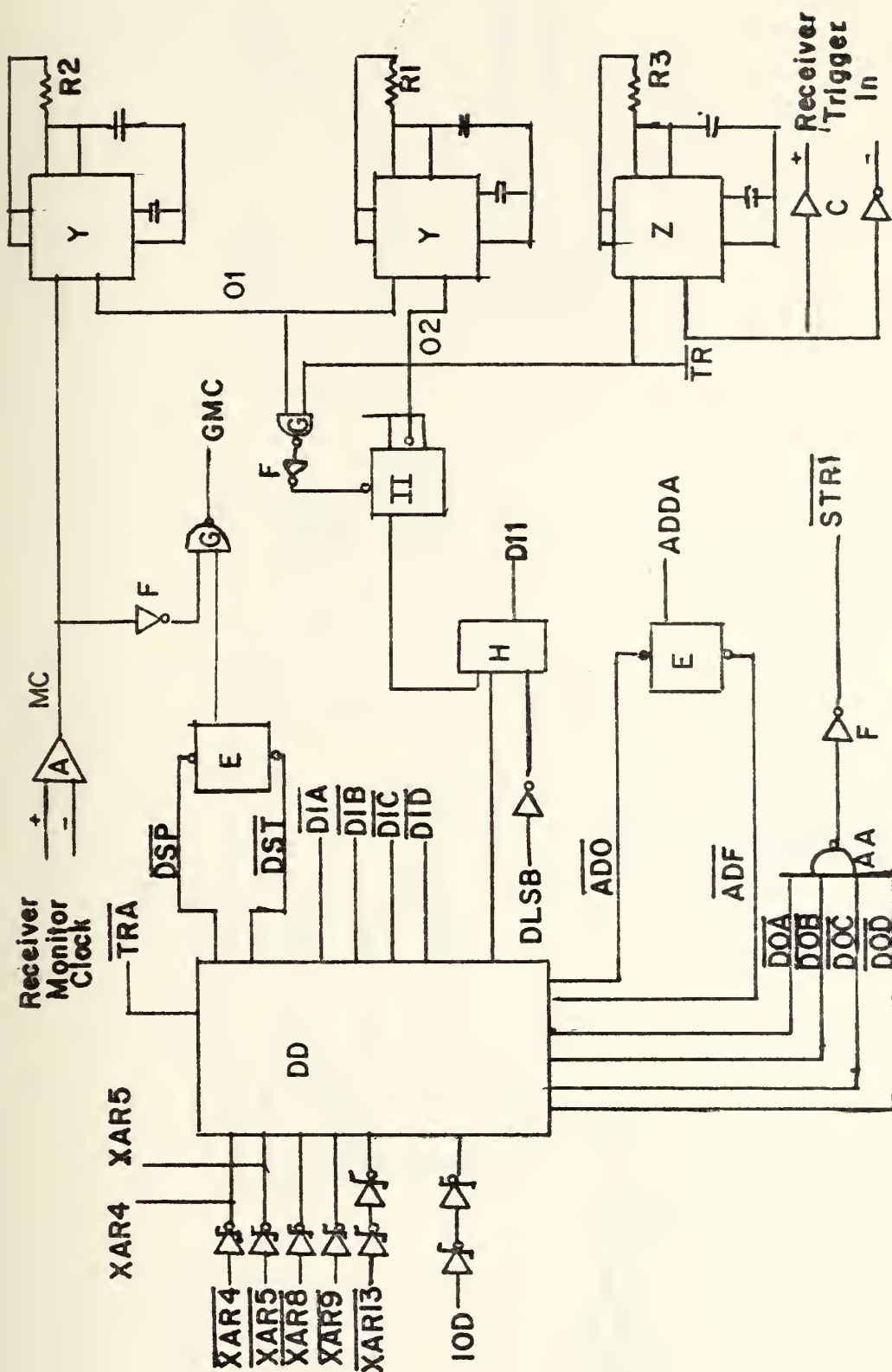


Figure 5
Interface Control Section (Part I)

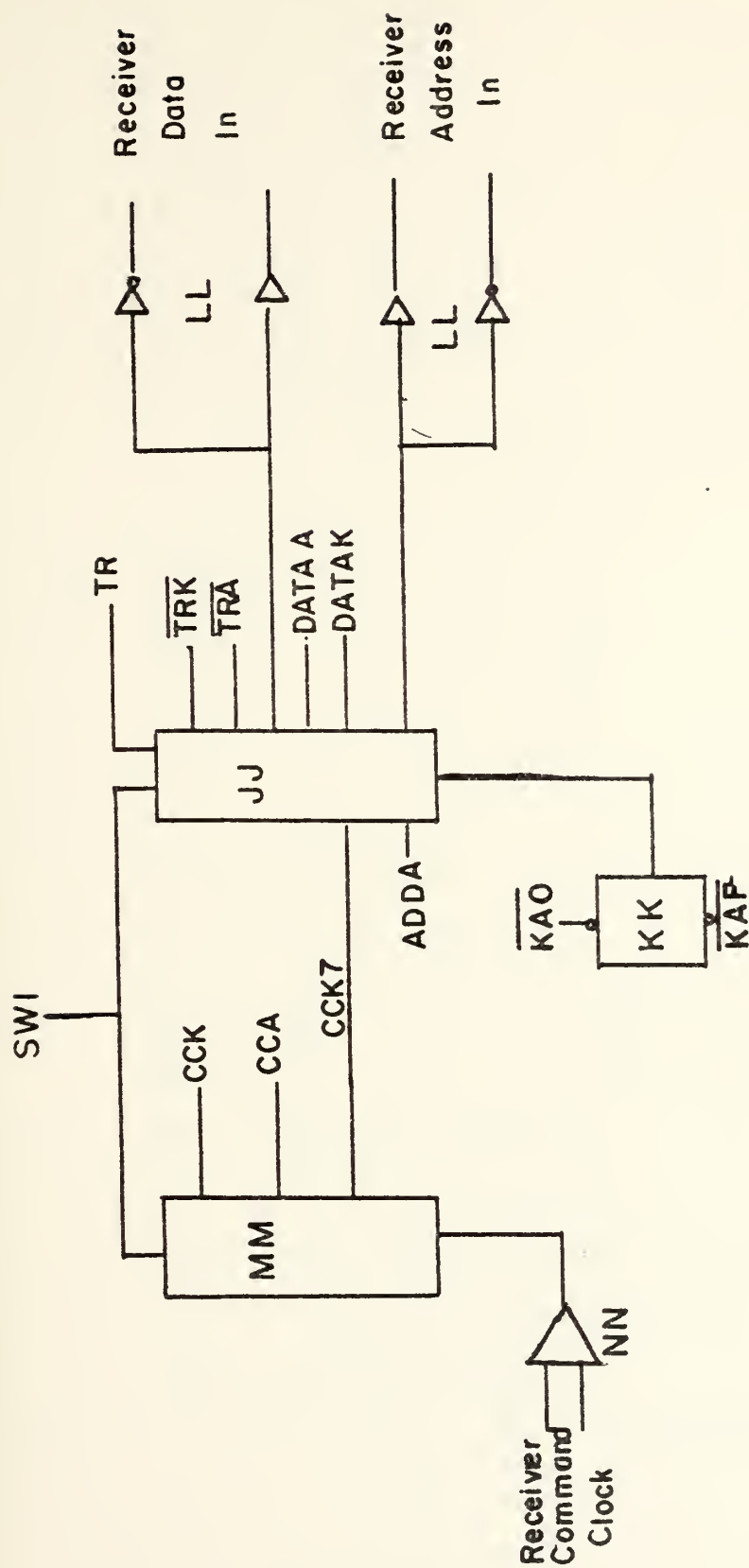


Figure 6
Interface Control Section (Part 2)

| | | |
|--|------|--------------------------------------|
| TRA | [0] | ATAC Trigger Command |
| DIA | [1] | Receiver input word one |
| DIB | [2] | Receiver input word two |
| DIC | [3] | Receiver input word three |
| DID | [4] | Receiver input word four |
| DSP | [5] | Stop Monitor Clock |
| DST | [6] | Start Monitor Clock |
| ADD | [7] | ATAC - Address on |
| DOA | [8] | Receiver output word one |
| DOR | [9] | Receiver output word two |
| DOR | [9] | Receiver output word two |
| DOC | [10] | Receiver output word three |
| DOD | [11] | Receiver output word four |
| RDY | [12] | Read D11 for ready signal |
| ADF | [15] | ATAC - Address off |
| TR | | Receiver trigger |
| CCK | | KIM-1 Command Clock |
| CCK7 | | Control Line for KIM-1 Command Clock |
| CCA | | Command Clock for ATAC interface |
| ADDA | | ATAC - Receiver Address |
| KADD | | KIM-1 - Receiver Address |
| KAO | | KIM-1 - Receiver Address On |
| KAF | | KIM-1 - Receiver Address Off |
| TRK | | KIM-1 Trigger Command |
| (Numbers in brackets refer to ATAC XAR commands) | | |

Table III
Interface Command List

commands to IC-MM. This IC is a quad Tri-State buffer which is used to control the destination of the command clock. The switch opens buffer one which directs the command clock to the ATAC. The CCK7 line closes buffers two and four disabling the command clock input to the KIM-1.

The ATAC supplies the control section with six lines. Five of these are the XAR bits 4,5,8,9, and 13. Using 4, 5, 8, and 9 as address lines to pins 20-23 of IC-DD, a four-to-sixteen demultiplexer, sixteen (2^4) unique commands (Table III) were made available. The sixth line, the IOD, and XAR 13 were used as strobes or enables for the demultiplexer. In this way XAR 13 was able to specify this receiver uniquely, and the IOD ensured that addresses and data were stable before passing a command. When both IOD and XAR 13 are low, IC-DD is operational and the output corresponding to the address on pins 20-23 is forced low. At any time that either or both the two strobe lines are high, all outputs of IC-DD are held high and no commands are generated, regardless of the activity on pins 20-23.

At the beginning of the Receiver Control program (Chapter IV), the ATAC sends commands to address the receiver (ADD) and to open the gate for the monitor clock (DST). ADD places a low on pin 2 of IC-E, setting the flip-flop and forcing the ADDA line high. This line activates the receiver's I/O through ICs -JJ, -A, -B, -C, and -LL, as described above. The DST command is passed to

pin 7 of IC-E. This sets this flip-flop and allows the monitor clock (MC) to shift data from the receiver into the storage register during every period two of the receiver's cycle. The MC line is also connected directly to a timing circuit. This circuit produces the pulse described in the early part of this chapter. The first of a pair of monostable multivibrators, IC-Y (Figure 5) is triggered by the first clock pulse of MC. IC-Y outputs a pulse, interval A of timing diagram (Figure 7), which triggers the second. The second's output, interval B, is connected to pin 1 of IC-II, a negative-edge triggered, J-K flip-flop. This IC is wired so that it is set on the output of the second multivibrator and reset by either the the output of the first multivibrator or the command TR. The output of this flip-flop, pin 15, is called the RLP. This line is multiplexed with the least significant bit of the output register and inverted by IC-H for use by the ATAC on line D11.

The RLP pulse is adjustable through variable resistors (trimmers) one and two. Trimmer one controls interval B and trimmer two interval A. In effect, trimmer two varies the position of the pulse and trimmer one its width. The placement and width are the key to proper operation of the interface. The pulse must remain in period three. Although some overlap into period four is allowable it is not desirable, and any overlap into period two could cause

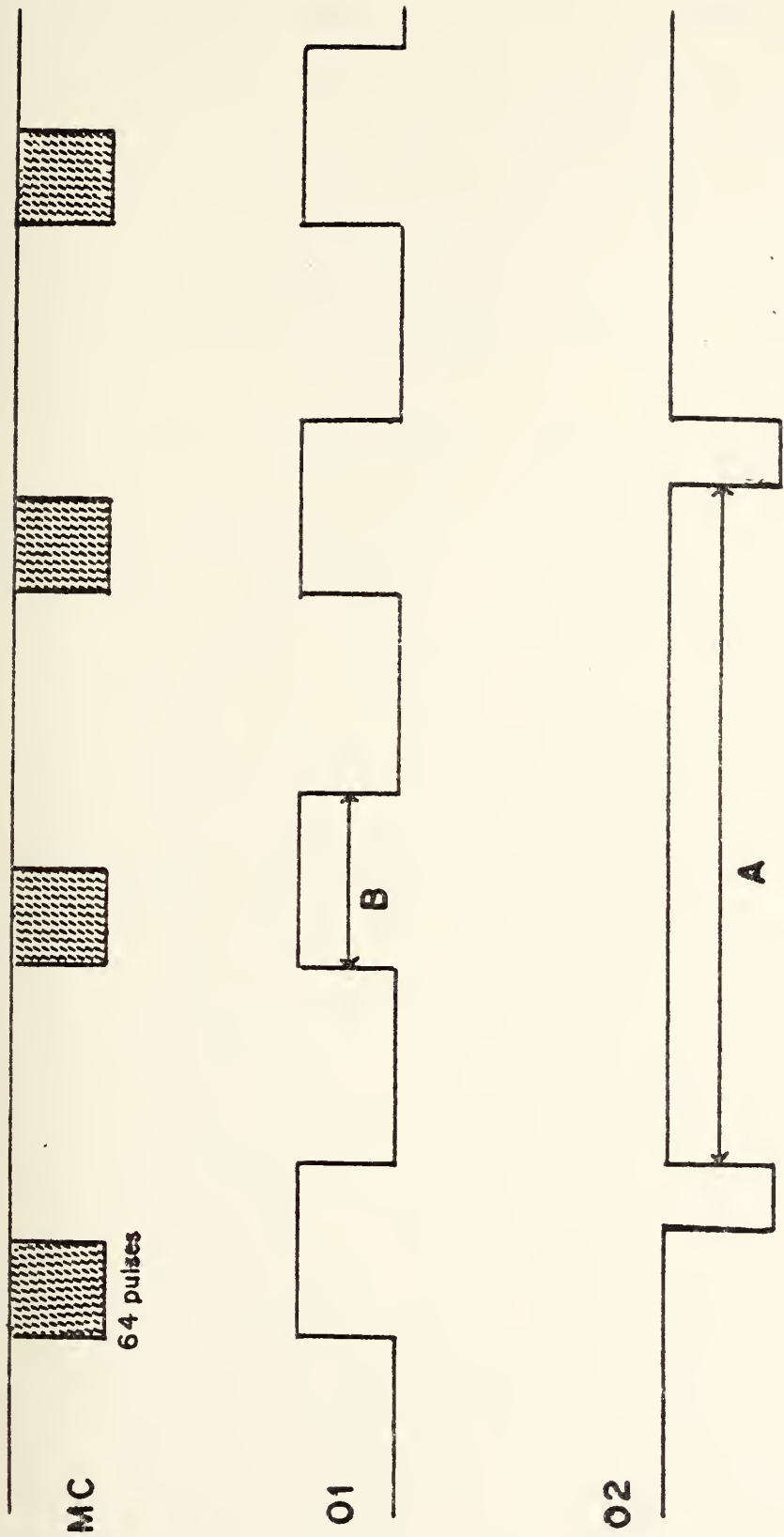


Figure 7
Interface Timing Diagram

incorrect operation. At present, the RLP pulse is programmed for every other period three. This allows the receiver to stabilize between samples taken by the computer. If more or less time is desired, the pulse can be set in every, every other, or every third period three by varying trimmer one. Greater time between pulses can be achieved by changing the .47 uF capacitor (6H-7,8) to one of larger value.

When the ATAC is ready to send a word, it loads the input register of the interface using commands DIA, DIB, DIC, DID, and then waits for a high on the D11 line. When RLP is low, D11 is high and the ATAC sends command IRA. This command is routed to a separate monostable multivibrator, IC-Z, by way of multiplexer IC-JJ. The timing circuit provides the trigger pulse in period four which changes the receiver's mode to remote active. It also sets RLP high to prevent any interaction with the ATAC until this cycle of the receiver is complete. During the following period one, the receiver sends the command clock to the input register via ICs -JJ and -MM, and inputs the data word through ICs -IJ and -LL. Meanwhile, the ATAC is waiting for RLP to go low again. When it does, the ATAC closes the MC gate with a DSP command and loads four sixteen-bit words with commands DDA, DDB, DDC, and DDD. Once the receiver word is stored in the ATAC, a DSIA command is sent to open the MC gate. When the operator has finished execution of the Receiver Control program and exits, the ATAC

sends the interface commands ADF and DSP to turn off the address line to the receiver and close the MC gate. The interface is now back in a stand-by status.

In order to set up the interface for operation with the KIM-1, the reset button must be pushed and the computer switch placed in the KIM-1 position. The reset button is unique to KIM-1 interface operation, and is necessary because of the use of the KIM-1's non-maskable interrupt. This interrupt is used to synchronize the KIM-1 with the receiver's command clock. Pressing the reset button places a momentary low on pin 5 of IC-rK, the flip-flop that controls the receiver's address line from the KIM-1. This resets the flip-flop and insures that the command clock output is disabled until required. ICs -JJ and -MM now transfer data from the KIM-1 and not the ATAC. The CCK7 line follows the address line from IC KK and gates the command clock off and on at the proper time. When the KIM-1 is ready to send a word to the receiver, it waits for a low on the RLP line. This line is connected to the maskable interrupt line. This low generates an interrupt and places the KIM-1 in the output program. This routine provides a trigger pulse for the trigger timing circuit and outputs the data synchronously with the command clock. The difference between the ATAC and KIM-1 actions of the interface is due to the position of the switch. The only function the interface serves is to provide reliable and compatible data to the appropriate device, whether it is receiver or

computer.

B. INPUT/OUTPUT REGISTERS

These two registers are used for the ATAC only. The registers were designated input or output by their related function with the receiver. They were constructed to provide the necessary, temporary storage while converting parallel and serial data back and forth. Both registers are connected to the PIO bus, with the major difference being the Tri-State connections of the serial to parallel, or output register.

The input register (Figure 8) was the easier to implement. It consists of eight 8-bit shift registers with parallel input and serial output. The parallel input comes from the ATAC's PIO bus, which is buffered by schottky inverters to reduce noise. The lines are connected to the ICs in such a way as to load words into two adjacent shift registers simultaneously. This is possible because the shift registers will only latch data in when their respective load line is low. By proper connection of the DIA-DID lines to pin 1 of the ICs, and coordinating the commands with the data, the output register can be completely and correctly filled. The command clock from the receiver is connected to pin 15 of each of the eight registers. When it is present, it clocks the data through the register exiting through pin 16 of IC-VV. From here, it goes through the control section

at IC-MM and on to the receiver.

The output register (Figure 9) performs the reverse operation. However, in order to separate it into words that are short enough for the ATAC, the data has to be multiplexed before it can be connected to the PIO bus. The Tri-State multiplexers, ICs -I through -L and -U through -X, and the required buffers, ICs -EE through -GG, were used to prevent interaction with the PIO bus when not in use. The timing here is more critical than in the input register system. Before the ATAC begins a read cycle from the output register, the clock signal to the register is stopped (DSP). This prevents the ATAC from reading non-stationary data. All the Tri-State multiplexers are addressed by connecting XAR bits 4 and 5 to pins 2 and 4 respectively. The commands DQA-DDD are ANDed together (NANDed and inverted) and the output connected to all the multiplexers as strobes at pins 1 and 15. When the ATAC reads a word, the XAR bits select the word and the strobe produces it during the microsecond when the PIO bus is available.

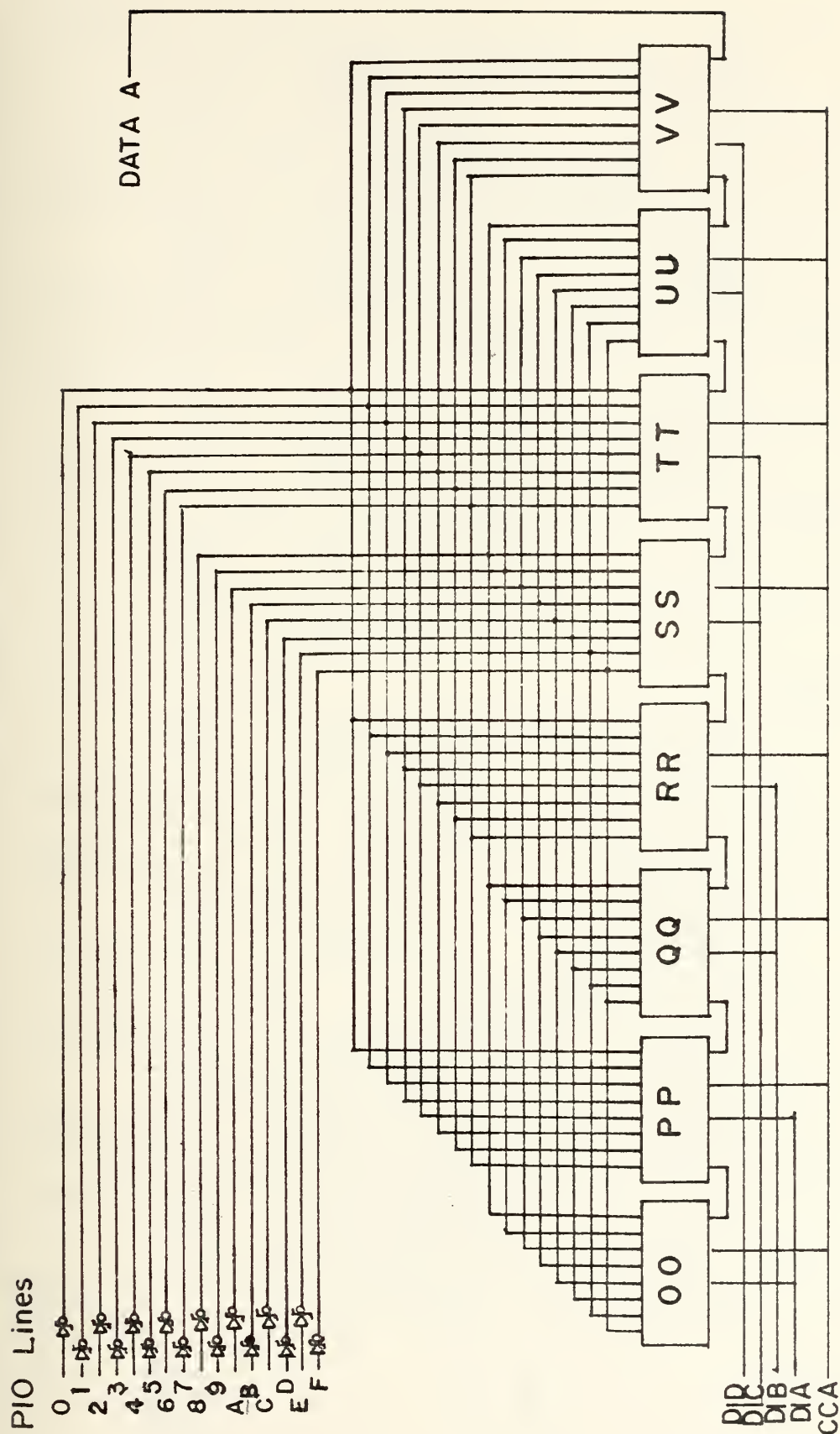


Figure 8
Input Register

P10 Lines

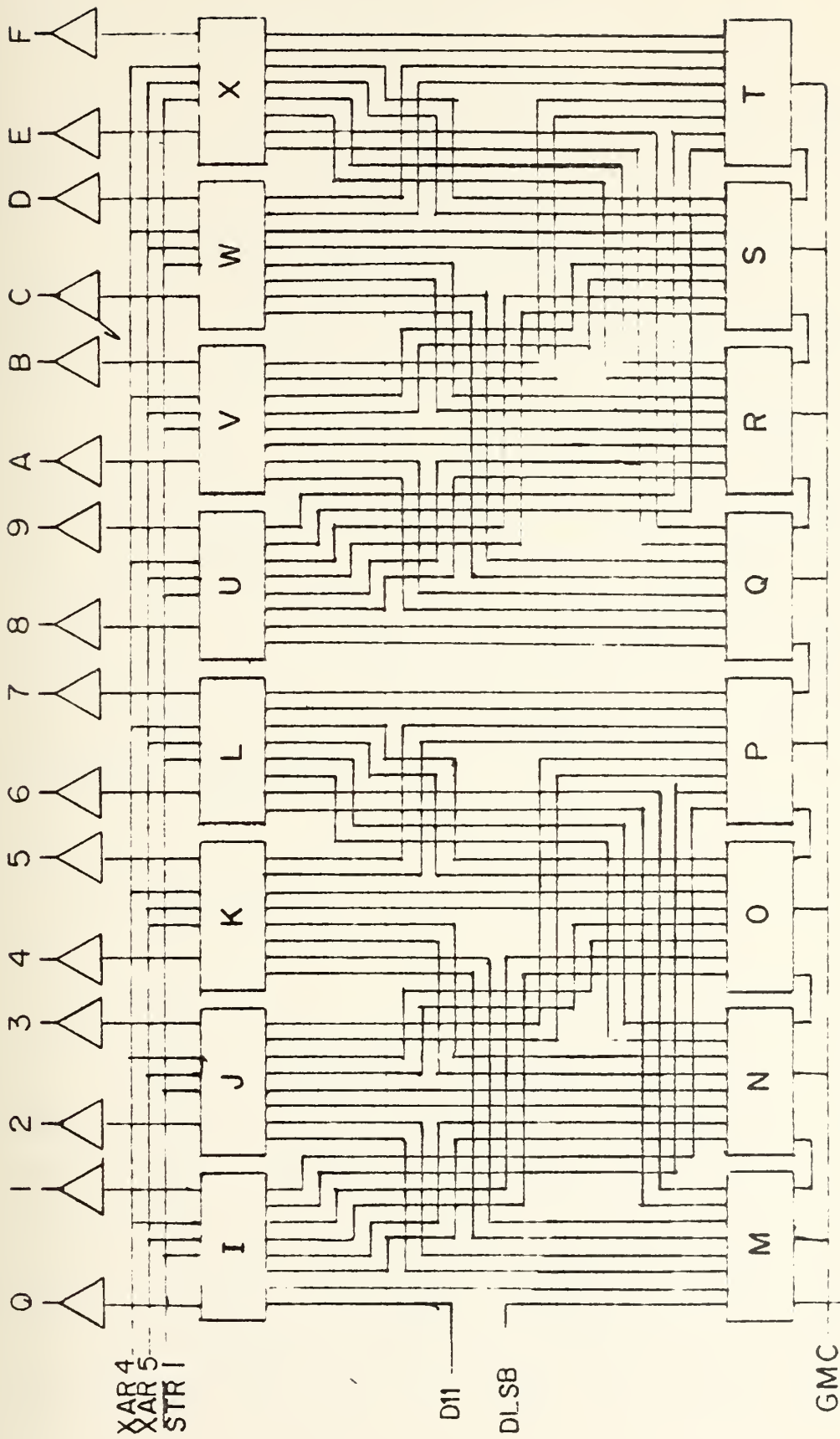


Figure 9
Output Register

Receiver
Data
Out

V. THE PROGRAM

The ATAC program was written in two major sections; a system monitor and a control. The monitor is called the Main System and provides the operator the ability to program the ATAC from the operator's terminal. Receiver Control commands the interface and, therefore the receiver. Both programs were initially written prior to the construction of the interface, so many modifications were made using the Main System and its subroutines. After the interface was built and tested and the Receiver Control section modified to correctly control the tuning of the receiver, the complete program was saved on paper tape (Appendix C). Operation of the computer is discussed in Appendix B and a sample run can be found in Appendix F.

A. THE MAIN SYSTEM

The Main System section consists of a small executive and a group of interconnected subroutines (Figure 10). The executive provides a basis for the subroutines when the receiver control program is not being executed. It is these subroutines that control the input and output to the operator terminal. The input routine is called KEYMR and the output routine, OUTPUT. OUTPUT converts correctly-formatted computer words into ASCII and displays them on the

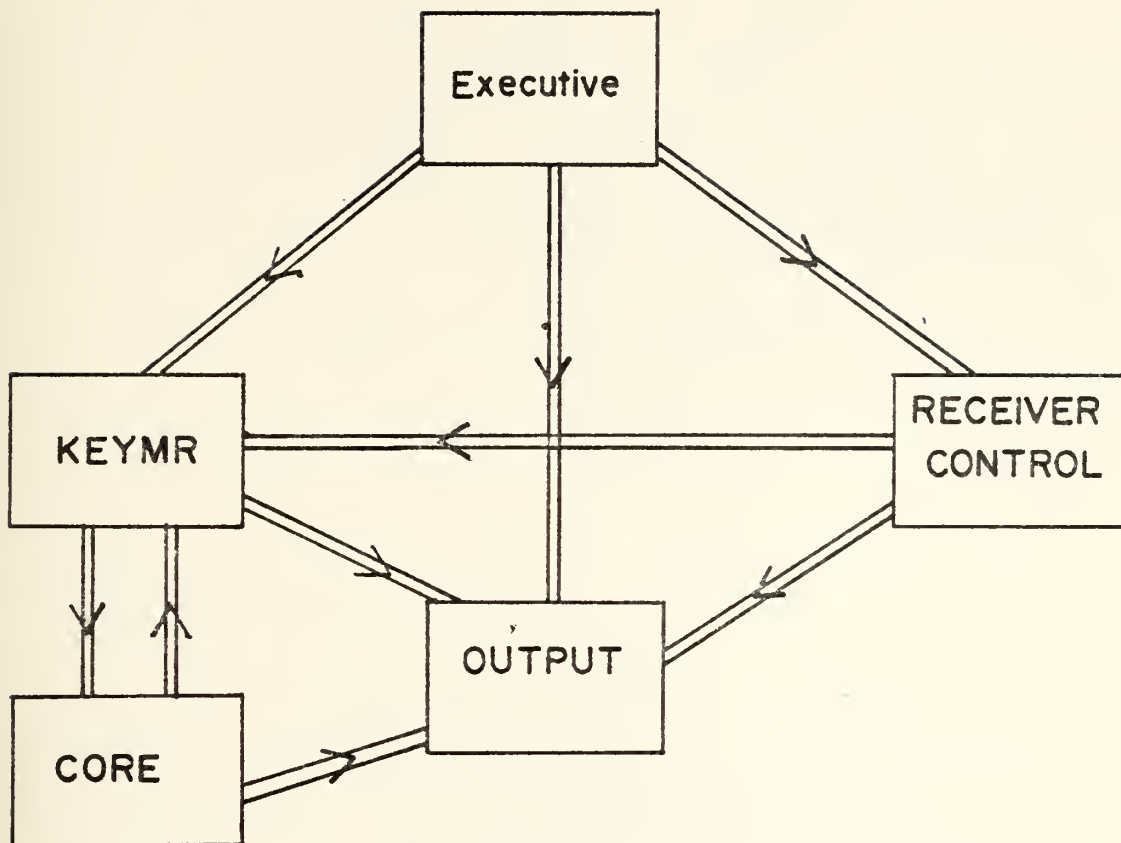


Figure 10
ATAC Program Block Diagram

I. CORE Commands

- CO -- calls CORE from KEYMR.
- a. DU 'address' -- displays 80 memory locations beginning with 'address'.
 - b. DI 'address' -- displays the contents of memory location 'address'.
 - c. CH 'address' 'value' -- Replaces the contents of memory at 'address' with 'value'.
 - d. CS 'address' -- Beginning at 'address', the contents of memory are replaced with the values typed on the lines following the command. Exit is accomplished by command DN.
 - e. DN -- Returns execution to CORE if in CS, otherwise returns to calling routine.

II. Receiver Control Commands

- WJ -- Calls Receiver Control from the executive.
- a. 0 -- Set-up - Routine to input values for entry into Receiver.
 - b. 1 -- Displays set-up control word.
 - c. 2 -- Displays last control word sent to receiver.
 - d. 3 -- Displays last control received from receiver.
 - e. 4 -- Sends set-up control word to receiver.
 - f. 5 -- Routine to input scan variables and execute a scan.
 - g. 6 -- Receive and Display control word from the receiver.
 - h. 7 -- Exit program and return to caller.
 - i. 8 -- Reinitialize program as if entering.

Table IV
AFAC Program Commands

terminal. KEYMR does the reverse, and stores the input in a buffer for use by the caller. KEYMR and OUTPUT were programmed to accept and display only uppercase letters, numerals, and a small number of needed symbols. But, because of the method employed to convert ASCII to machine code, it was found that each lower case letter entered from the keyboard was automatically mapped into its respective upper case twin. This relieves the operator of the responsibility of using the shift key. A part of the KEYMR, called COPE, is available for use by the operator to display and/or change sections of memory. The four available commands in this routine and their functions are displayed in Table IV. Care must be taken not to change memory locations which are used by the Main System. This could result in complete erasure of the ATAC's memory. Without KEYMR, OUTPUT, and COPE, or routines similar to them, it would have been extremely difficult to perform any amount of troubleshooting or modification of the Receiver Control section.

B. RECEIVER CONTROL

This section of the system is a branch of the executive. Its main objective is to control both outputs and inputs of the interface from the operator's terminal. To assist those operators with little experience in this system, the Receiver Control section is equipped with uncomplicated

instructions and program safeguards. This produces almost foolproof operation but, it does so at the expense of program simplicity. Discussion of this section is separated into two parts. First a broad description of the complete section is discussed, followed by a detailed look at the two subroutines which interact with the interface.

When the Receiver Control program is entered, it performs five important actions. It initializes all necessary flags; enables the receiver and opens the MC gate; sends and receives a complete receiver word; and displays the instruction set to the operator. After this, it calls on KEYMR and waits for a command. When an input is delivered, the program checks its legality. If it is not a valid command, KEYMR is called again.

A valid command is a numeral between zero and eight (Table IV). These can be separated for discussion into three groups. The display group (0-3) inputs and exchanges information with the operator. The receiver group (4-6) performs operations with the receiver. The final group of commands (7-8) are used to exit or reinitialize the program. Group one has one input and three display commands. Command zero instructs the operator to input the parameters desired. It stores these parameters in memory in the display format, as opposed to control word format. Commands one, two, and three all display parameters. One displays the last parameters set-up by command zero. Two displays the last parameters sent to the receiver. Three displays the last

word received from the receiver. Commands seven and eight make up group three. Seven exits the program entirely and returns to the executive after disabling the receiver. Eight, on the other hand, returns the program to its beginning as if it had just been entered.

The remaining three commands are the most important. Group two commands control the actions of the interface. Command four converts the parameters set-up by command zero into control word format. It then calls the I/O subroutine described below, and outputs and inputs a receiver word. To merely receive a word from the receiver, command six is used. The program calls the input subroutine below and then exits to command three to display the parameters received. Command five scans a band of frequencies selected by the operator in search of a specified signal strength. All other parameters remain the same as those set-up by command zero.

With the exception of the instructions executed when entering and exiting Receiver Control, complete control of the interface and the receiver is resident in approximately forty computer instructions. These forty are grouped into the two subroutines WJR and WJS. WJS sends words to the receiver and WJR receives them. WJS loads the information and addresses to be sent to the receiver into the computer registers. The addresses are then matched to a word of data and sent to the interface input register. The routine now waits for the appropriate signal generated by RLP. When

this is received, a trigger command is sent to load the word into the receiver. At this point the routine checks the value of a counter. This test is to prevent the computer entering an infinite loop if either the interface or receiver is not turned on. If the test is unsatisfactory, the routine prints:

INFINITE LOOP
PLEASE CHECK RECEIVER AND INTERFACE

and reverts to operator control. If the test is satisfactory, the subroutine automatically continues to WJR. WJR loads another set of addresses into the computer registers. Here, a short wait for the RLP signal is necessary before any action is taken. The MC gate is closed immediately upon receipt of this signal. The receiver word is then loaded into the ATAC by outputting the address on the XAP lines and reading the data on the PIO lines. When the complete word is received, the MC gate is opened. At this point it is necessary to test for command six. This test determines whether the computer is sending and receiving or only receiving. If the execution of both WJS and WJR is being performed, a comparison between the word sent and the word received is necessary. This comparison is skipped if the computer is only executing WJR (command six). The first three control words sent by WJS and received by WJR are used for this comparison, when it is performed. If

any words differ, the computer returns to WJS to repeat the cycle until one of two conditions are met: either the words match or the WJS counter test discussed earlier fails. If the words match, WJP continues on to convert the received control words into the display format and then returns to the caller.

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VI. RECOMMENDATIONS

The system as it stands now is but a beginning. Additions and modifications for future work should include; A/D converters for the receiver outputs; Morse and/or teletype decoders; and an expansion of the computer program. Implementation of either of the first two implies the third. There are some operator assistance program modifications that need to be made. The two that come immediately to mind are (1) a method to abort the scan routine from the operator's console, and (2) the ability to change individual parameters in addition to the set-up command already located in the program. Addition of the A/D converters implies a program increase to decode and process this new data. Switching routines and probably some hardware will be needed for the decoders. The capabilities of the system are limited only by the abilities of the operator and programmer.

VII. CONCLUSION

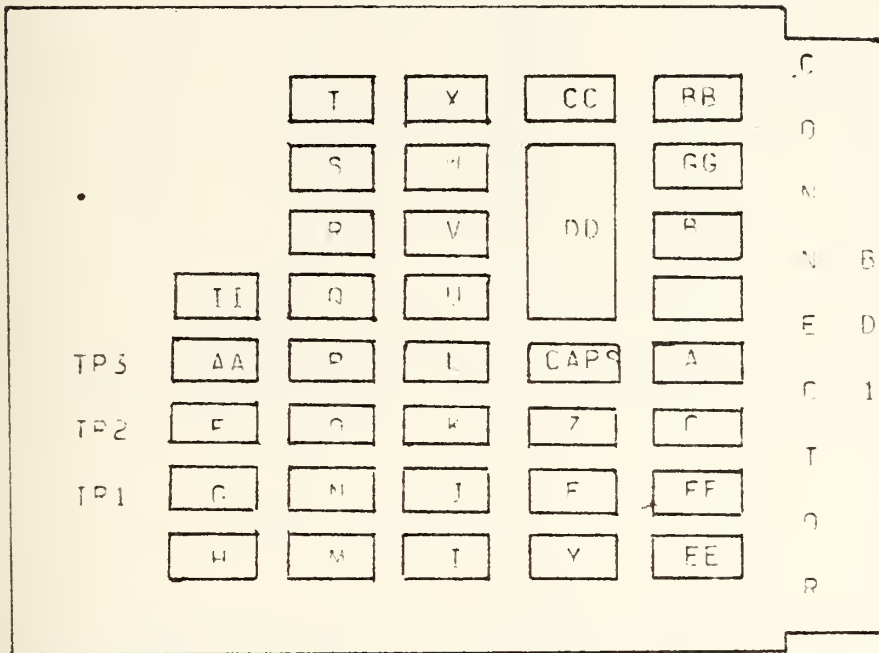
As long as the computer requires only that data obtained from the receiver's word, the interface is flexible enough to provide reliable results. At this time there are no known "bugs" in either the interface or the program. Both have been thoroughly tested to provide the operator with the most dependable system possible.

APPENDIX A

INTERFACE WIRING

A. Board 1

Integrated circuit locations (from Top of board).



Integrated Circuit

| pin | A | B | C | F | F | G | H |
|-----|---------|--------|---------|-------|---------|--------|--------|
| 1 | 8820 | 8820 | 8830 | 7476 | 7404 | 7400 | 74157 |
| 1 | RD1-M | RD1-K | C-2/Z-3 | VCC | M-3 | RD2-E | DD-14 |
| 2 | nc | nc | C-1/C-3 | DD-8 | H-3 | Y-9 | II-14 |
| 3 | BD1-L | BD1-J | C-2/C-4 | DD-15 | A-6/Y-8 | F-11 | F-2 |
| 4 | nc | nc | C-3 | VCC | G-5 | E-11 | I-6 |
| 5 | nc | nc | RD1-F | VCC | AA-6 | F-4 | nc |
| 6 | F-3/Y-8 | M-1 | BD1-H | VCC | G-12 | F-9 | nc |
| 7 | GRD | GRD | GRD | DD-7 | GRD | GRD | GRD |
| 8 | nc | nc | nc | DD-6 | I-8 | nc | nc |
| 9 | nc | nc | nc | VCC | G-6 | nc | nc |
| 10 | nc | nc | nc | nc | II-3 | nc | nc |
| 11 | nc | nc | nc | G-4 | G-3 | F-13 | nc |
| 12 | nc | nc | nc | VCC | I-15 | F-6 | nc |
| 13 | nc | nc | nc | GRD | G-11 | DD-14 | GRD |
| 14 | VCC | VCC | VCC | nc | VCC | VCC | VCC |
| 15 | XXXXXX | XXXXXX | XXXXXX | RD1-Y | XXXXXX | XXXXXX | XXXXXX |
| 16 | XXXXXX | XXXXXX | XXXXXX | VCC | XXXXXX | XXXXXX | XXXXXX |

Integrated Circuit

| pin | I | J | K | L | M | N |
|-----|----------|----------|----------|----------|------------|------------|
| | 7214 | 7214 | 7214 | 7214 | 74164 | 74164 |
| 1 | X-15 | I-15/J15 | J-15/K15 | K-15/L15 | B-6/M-2 | M-13/N-2 |
| 2 | DD22/J-2 | I-2/K-2 | J-2/L-2 | K-2/U-2 | M-1 | N-1 |
| 3 | S-3 | S-5 | S-10 | S-12 | F-1 | U-6 |
| 4 | Q-3 | Q-5 | Q-10 | Q-12 | I-10 | U-10 |
| 5 | Q-3 | Q-5 | Q-10 | Q-12 | J-6 | V-6 |
| 6 | H-4 | M-5 | M-10 | M-12 | J-10 | V-10 |
| 7 | EE-1 | FE-5 | EF-11 | FF-1 | GRD | GRD |
| 8 | GRD | GRD | GRD | GRD | M-8 | M-8/O-8 |
| 9 | EE-3 | FE-9 | EF-13 | FF-3 | VCC | VCC |
| 10 | M-4 | M-6 | M-11 | M-13 | K-6 | W-6 |
| 11 | Q-4 | Q-6 | Q-11 | Q-13 | K-10 | W-10 |
| 12 | Q-4 | Q-6 | Q-11 | Q-13 | L-6 | X-6 |
| 13 | S-4 | S-6 | S-11 | S-13 | L-10 | X-10 |
| 14 | DD23/J14 | K-14/I14 | J-14/L14 | K-14/U14 | VCC | VCC |
| 15 | F-12/J-1 | J-1/K-1 | K-1/L-1 | L-1/U-1 | XXXXXXXXXX | XXXXXXXXXX |
| 16 | VCC | VCC | VCC | VCC | XXXXXXXXXX | XXXXXXXXXX |

| pin | U | P | Q | R | S | T |
|-----|----------|----------|----------|----------|----------|----------|
| | 74164 | 74164 | 74164 | 74164 | 74164 | 74164 |
| 1 | N-13/O-2 | O-13/P-2 | P-13/Q-2 | Q-13/R-2 | R-13/S-2 | S-13/T-2 |
| 2 | Q-1 | P-1 | Q-1 | R-1 | S-1 | T-1 |
| 3 | I-5 | U-5 | I-4 | U-4 | I-3 | U-3 |
| 4 | I-11 | U-11 | I-12 | U-12 | I-13 | U-13 |
| 5 | J-5 | V-11 | J-4 | V-4 | J-3 | V-3 |
| 6 | J-11 | V-11 | J-12 | V-12 | J-13 | V-13 |
| 7 | GRD | GRD | GRD | GRD | GRD | GRD |
| 8 | N-8/P-8 | O-8/Q-8 | P-8/R-8 | Q-8/S-8 | R-8/T-8 | S-8/F-8 |
| 9 | VCC | VCC | VCC | VCC | VCC | VCC |
| 10 | K-5 | W-5 | K-4 | W-4 | K-3 | W-3 |
| 11 | K-11 | W-11 | K-12 | W-12 | K-13 | W-13 |
| 12 | L-5 | X-5 | L-4 | X-4 | L-3 | X-3 |
| 13 | L-11 | X-11 | L-12 | X-12 | L-13 | X-13 |
| 14 | VCC | VCC | VCC | VCC | VCC | VCC |

Integrated Circuit

| | U | V | W | X | Y | Z |
|-----|----------|----------|----------|----------|------------|------------|
| pin | 7214 | 7214 | 7214 | 7214 | 556 | 555 |
| 1 | L-15/U15 | U-15/V15 | V-15/W15 | W-15/X15 | Y2/Tp-1A | GRD |
| 2 | L-2/V-2 | U-2/W-2 | V-2/X-2 | W-2 | Y-1/HH-6 | PD1-E |
| 3 | T-3 | T-5 | T-10 | T-12 | HH-5 | C-1 |
| 4 | P-3 | R-5 | P-10 | R-12 | VCC | VCC |
| 5 | P-3 | P-5 | P-10 | P-12 | BD1C/II1 | HH-1 |
| 6 | N-3 | N-5 | N-10 | N-12 | Y-9 | HH-2/Z-7 |
| 7 | FF-5 | FF-11 | GG-1 | GG-5 | GRD | Z6/Tp-3A |
| 8 | GRD | GRD | GRD | GRD | A-6/F-3 | VCC |
| 9 | FF-9 | FF-13 | GG-3 | GG-9 | Y-6 | XXXXXXXXXX |
| 10 | N-4 | N-6 | N-11 | N-13 | VCC | XXXXXXXXXX |
| 11 | P-4 | P-6 | P-11 | P-13 | HH-4 | XXXXXXXXXX |
| 12 | R-4 | R-6 | R-11 | R-13 | Y-13/HH4 | XXXXXXXXXX |
| 13 | T-4 | T-6 | T-11 | T-13 | Y12/Tp2A | XXXXXXXXXX |
| 14 | L-14/V14 | U-14/W14 | V-14/X14 | W-14 | VCC | XXXXXXXXXX |
| 15 | U-1/V-1 | V-1/W-1 | W-1/X-1 | X-1/[-1 | XXXXXXXXXX | XXXXXXXXXX |
| 16 | VCC | VCC | VCC | VCC | XXXXXXYYXX | XXXXXXXXXX |

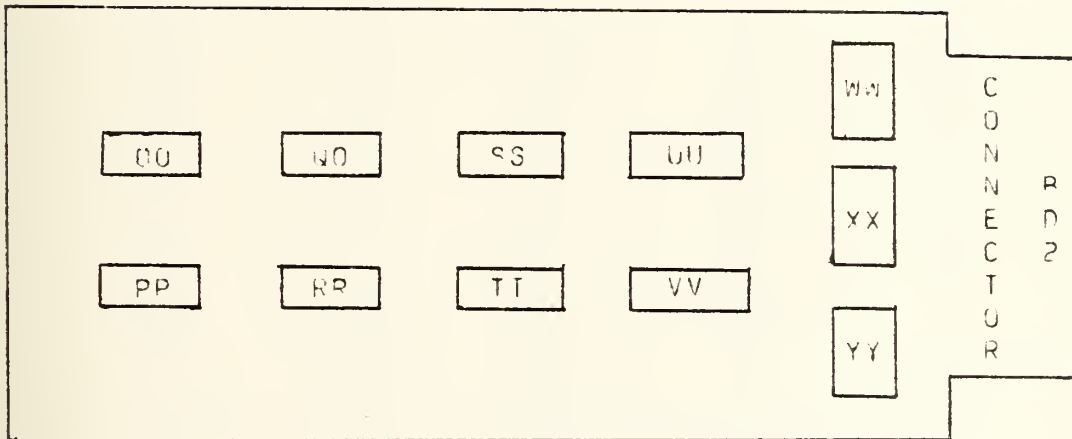
| | AA | BB | CC | EE | FF | GG | HH | II |
|-----|----------|----------|----------|----------|----------|----------|----------|------|
| pin | 7420 | 74s04 | 74s04 | 7407 | 7407 | 7407 | CAPS | 7476 |
| 1 | DD-10 | CC-6 | BD1-17 | T-7 | L-7 | W-7 | Z-5 | Y-5 |
| 2 | DD-11 | DD-18 | DD-23 | BD1-1 | BD1-7 | PD1-13 | Z-6 | VCC |
| 3 | nc | BD1-21 | nc | I-9 | L-9 | W-9 | Y-11 | F-10 |
| 4 | DD-9 | BB-5 | nc | BD1-2 | BD1-8 | PD1-14 | Y-12 | VCC |
| 5 | DD-13 | BB-4 | BD1-T | J-7 | U-7 | X-7 | Y-3 | nc |
| 6 | F-5 | DD-19 | BB-1 | BD1-3 | BD1-9 | BD1-15 | nc | nc |
| 7 | GRD | GRD | GRD | GRD | GRD | GRD | Y-2 | nc |
| 8 | nc | DD-20 | nc | BD1-4 | BD1-10 | BD1-16 | GRD | nc |
| 9 | nc | BD1-20 | nc | J-9 | U-9 | X-9 | GRD | nc |
| 10 | nc | DD-21 | nc | BD1-5 | BD1-11 | nc | GRD | nc |
| 11 | nc | BD1-19 | nc | K-7 | V-7 | nc | GRD | nc |
| 12 | nc | DD-22 | nc | BD1-6 | BD1-12 | nc | GRD | nc |
| 13 | nc | BD1-18 | nc | K-9 | V-9 | nc | GRD | GRD |
| 14 | VCC | VCC | VCC | VCC | VCC | VCC | GRD | H-2 |
| 15 | XXXXXXXX | XXXXXXXX | XXXXXXXX | XXXXXXXX | XXXXXXXX | XXXXXXXX | XXXXXXXX | nc |
| 16 | XXXXXXXX | XXXXXXXX | XXXXXXXX | XXXXXXXX | XXXXXXXX | XXXXXXXX | XXXXXXXX | GRD |

Integrated Circuit

| | | DD | |
|-----|-------|-------|-----------|
| | | 74154 | |
| pin | | pin | |
| 1 | RD1-P | 13 | AA-5 |
| 2 | RD1-X | 14 | G-13/H-1 |
| 3 | RD1-V | 15 | nc |
| 4 | RD1-V | 16 | nc |
| 5 | RD1-U | 17 | F-3 |
| 6 | E-8 | 18 | BR-2 |
| 7 | E-7 | 19 | BR-6 |
| 8 | E-2 | 20 | GR-8 |
| 9 | AA-4 | 21 | BR-10 |
| 10 | AA-1 | 22 | BR-12/I-2 |
| 11 | AA-2 | 23 | CC-2/I-14 |
| 12 | GRD | 24 | VCC |

II. Board 2

Integrated Circuit locations (from Top of board)



Integrated Circuit

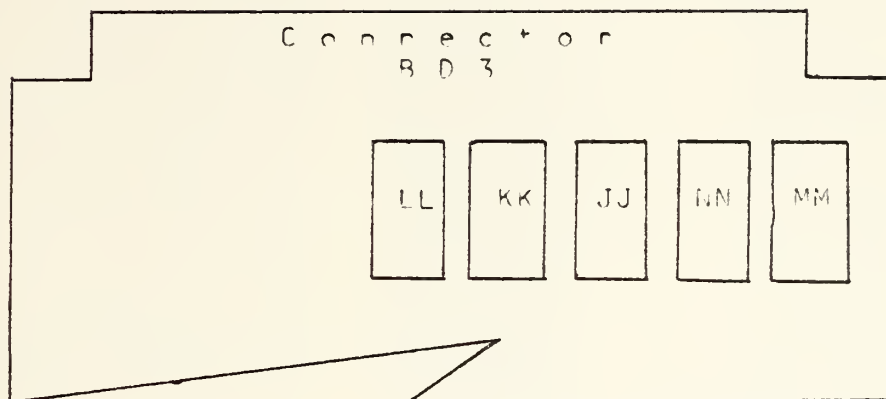
| pin | 00 | PP | QQ | RR | SS | TT |
|-----|--------|--------|--------|--------|--------|--------|
| 1 | BD2-A | BD2-A | BD2-B | BD2-B | BD2-C | BD2-C |
| 2 | GRD | GRD | GRD | GRD | GRD | GRD |
| 3 | WW-2 | YY-6 | WW-2 | YY-6 | WW-2 | YY-6 |
| 4 | WW-12 | YY-4 | WW-12 | YY-4 | WW-12 | YY-4 |
| 5 | WW-4 | YY-2 | WW-4 | YY-2 | WW-4 | YY-2 |
| 6 | WW-10 | YY-10 | WW-10 | YY-10 | WW-10 | YY-10 |
| 7 | nc | nc | nc | nc | nc | nc |
| 8 | GRD | GRD | GRD | GRD | GRD | GRD |
| 9 | PP-10 | QQ-10 | PP-10 | SS-10 | TT-10 | UU-10 |
| 10 | VCC | QQ-9 | PP-9 | QQ-9 | RR-9 | SS-9 |
| 11 | XX-2 | XX-4 | XX-2 | XX-4 | XX-2 | XX-4 |
| 12 | XX-12 | XX-6 | XX-12 | XX-6 | XX-12 | XX-6 |
| 13 | WW-8 | XX-8 | WW-8 | XX-8 | WW-8 | XX-8 |
| 14 | WW-6 | YY-12 | WW-6 | YY-12 | WW-6 | YY-12 |
| 15 | BD2-21 | BD2-21 | BD2-21 | BD2-21 | BD2-21 | BD2-21 |
| 16 | VCC | VCC | VCC | VCC | VCC | VCC |

| pin | UU | VV | WA | YX | YY |
|-----|--------|--------|------------|------------|------------|
| 1 | BD2-D | BD2-D | BD2-17 | BD2-10 | BD2-4 |
| 2 | GRD | GRD | QQ-3 * | QQ-11 * | PP-5 * |
| 3 | WW-2 | YY-6 | BD2-15 | BD2-9 | BD2-3 |
| 4 | WW-12 | YY-4 | QQ-5 * | PP-11 * | PP-4 * |
| 5 | WW-4 | YY-2 | BD2-13 | BD2-8 | BD2-2 |
| 6 | WW-10 | YY-10 | QQ-14 * | PP-12 * | PP-3 * |
| 7 | nc | nc | GRD | GRD | GRD |
| 8 | GRD | GRD | QQ-13 * | PP-13 * | nc |
| 9 | VV-10 | BD2-K | BD2-12 | BD2-7 | nc |
| 10 | TT-9 | UU-9 | QQ-6 * | nc | PP-6 * |
| 11 | XX-2 | XX-4 | BD3-14 | nc | BD2-5 |
| 12 | XX-12 | XX-6 | QQ-4 * | QQ-12 * | PP-14 * |
| 13 | WW-8 | XX-8 | BD2-16 | BD2-11 | BD2-6 |
| 14 | WW-6 | YY-12 | VCC | VCC | VCC |
| 15 | BD2-21 | BD2-21 | XXXXXXXXXX | XXXXXXXXXX | XXXXXXXXXX |
| 16 | VCC | VCC | XXXXXXXXXX | XXXXXXXXXX | XXXXXXXXXX |

* - bus connection - only first connection shown

C. Board Three

Integrated circuit locations (from Top of board)



Integrated Circuit

| | JJ | KA | LL | NN | NN |
|-----|-----------|-----------|------------|------------|------------|
| pin | 74157 | 7476 | 8830 | 74126 | 8820 |
| 1 | BD3-1 | VCC | LL-2/JJ-7 | BD3-1 | BD3-5 |
| 2 | KK-15 | BD3-11 | LL-1/LL-3 | BD3-17 | nc |
| 3 | BD3-6 | BD3-12/20 | LL-2/LL-4 | BD3-16 | BD3-4 |
| 4 | LL-10 | nc | LL-3 | JJ-12 | nc |
| 5 | BD3-8 | VCC | BD3-7 | GRD | nc |
| 6 | BD3-9 | nc | BD3-10 | BD3-16 | MM-a |
| 7 | LL-1 | nc | GRD | GRD | GRD |
| 8 | GRD | nc | BD3-19 | BD3-2 | nc |
| 9 | BD3-13 | nc | BD3-18 | NN-8 | nc |
| 10 | BD3-14 | nc | LL11/JJ-4 | BD3-1 | nc |
| 11 | nc | nc | LL12/LL10 | BD3-3 | nc |
| 12 | MM13/MM-4 | nc | LL13/LL11 | MM-a | nc |
| 13 | GRD | GRD | LL-12 | JJ-12 | nc |
| 14 | KK-15 | nc | VCC | VCC | VCC |
| 15 | GRD | JJ-2/JJ14 | XXXXXXXXXX | XXXXXXXXXX | XXXXXXXXXX |
| 16 | VCC | nc | XXXXXXXXXX | XXXXXXXXXX | XXXXXXXXXX |

D. Connectors

Circuit Board Edge-connectors

| | RD1 | RD2 | RD3 |
|----|-------------------|--------------|----------------|
| 1 | EE-2/RD2-10/CN1-2 | GRD | SW1/JJ-1/MM-10 |
| 2 | EE-4/RD2-11/CN1-3 | YY-5/RD1-13 | MM-8/RD2-21 |
| 3 | EE-6/RD2-12/CN1-4 | YY-3/RD1-14 | MM-11/CN3-1 |
| 4 | EE-8/RD2-13/CN1-5 | YY-1/RD1-15 | NN-2/CN2-2 |
| 5 | EE10/RD2-17/CN1-6 | YY-11/RD1-16 | NN-1/CN2-3 |
| 6 | EE12/RD2-16/CN1-7 | YY-13/RD1-12 | JJ-3/RD1-Y |
| 7 | FF-2/RD2-15/CN1-8 | XX-9/RD1-11 | LL-5/CN2-10 |
| *8 | FF-4/RD2-14/CN1-9 | XX-5/RD1-10 | JJ-5/CN3-2 |
| 9 | FF-6/RD2-9/CN1-10 | XX-3/RD1-8 | JJ-6/RD2-K |
| 10 | FF-8/RD2-8/CN1-11 | XX-1/RD1-1 | LL-6/CN2-11 |
| 11 | FF10/RD2-7/CN1-12 | XX-13/RD1-2 | KK-2/CN3-3 |
| 12 | FF12/RD2-6/CN1-13 | WW-9/RD1-3 | KK-3/CN3-4 |
| 13 | GG-2/RD2-2/CN1-25 | WW-5/RD1-4 | JJ-9/RD1-E |
| 14 | GG-4/RD2-3/CN1-24 | WW-11/RD1-8 | JJ-10/RD1-P |
| 15 | GG-6/RD2-4/CN1-23 | WW-3/RD1-7 | IJ-11/CN3-5 |
| 16 | GG-8/RD2-5/CN1-22 | WW-13/RD1-6 | nc |
| 17 | CC-1/CN1-11 | WW-1/RD1-5 | nc |
| 18 | PP-13/CN1-17 | nc | LL-9/CN2-12 |
| 19 | PP-11/CN1-19 | nc | LL-8/CN2-11 |
| 20 | PP-9/CN1-16 | nc | KK-3/RESET |
| 21 | PP-3/CN1-20 | DD-15/RD3-2 | nc |
| 22 | nc | GRD | VCC |
| A | GRD | DD-1/RD1-X | GRD |
| B | nc | GG-1/RD1-W | nc |
| C | II-14/CN3-6 | SS-1/RD1-V | nc |
| D | nc | UU-1/RD1-U | nc |
| E | 7-2/RD3-13 | nc | nc |
| F | C-5/CN2-9 | nc | nc |
| H | C-6/CN2-8 | nc | nc |
| J | B-3/CN2-7 | nc | nc |
| K | B-1/CN2-6 | nc | nc |
| L | A-3/CN2-5 | nc | nc |
| M | A-1/CN2-4 | nc | nc |
| N | nc | nc | nc |
| P | DD-1/RD3-14 | nc | nc |
| R | nc | nc | nc |
| S | nc | nc | nc |
| T | CC-5/CN1-14 | nc | nc |
| U | DD-5/RD2-D | nc | nc |
| V | DD-4/RD2-C | nc | nc |
| W | DD-3/RD2-B | nc | nc |
| X | DD-2/RD2-A | nc | nc |
| Y | E-15/RD3-6 | nc | nc |
| Z | VCC | nc | nc |

Cabinet Connectors

| pin: | CN1 | CN2 | CN3 |
|------|-----------------|----------------|----------------|
| 1 | GRD | GRD | BD3-3 "CCK" |
| 2 | BD1-1 "PIO 0" | BD3-4 "CC - " | BD3-8 "DATA K" |
| 3 | BD1-2 "PIO 1" | BD3-5 "CC + " | BD3-11 "KAO" |
| 4 | BD1-3 "PIO 2" | BD1-M "MC - " | BD3-12 "KAF" |
| 5 | BD1-4 "PIO 3" | BD1-L "MC + " | BD3-15 "TRK" |
| 6 | BD1-5 "PIO 4" | BD1-K "DO - " | BD1-3 "PLD" |
| 7 | BD1-6 "PIO 5" | BD1-J "DO + " | nc |
| 8 | BD1-7 "PIO 6" | BD1-H "TR - " | nc |
| 9 | BD1-8 "PIO 7" | BD1-F "TR + " | nc |
| 10 | BD1-9 "PIO 8" | BD3-7 "DI - " | nc |
| 11 | BD1-10 "PIO 9" | BD3-10 "DI + " | nc |
| 12 | BD1-11 "PIO 10" | BD3-19 "AD - " | nc |
| 13 | BD1-12 "PIO 11" | BD3-18 "AD + " | nc |
| 14 | BD1-T "IOU" | nc | GRD |
| 15 | nc | nc | nc |
| 16 | BD1-17 "XAP 4" | nc | nc |
| 17 | BD1-18 "XAP 5" | nc | nc |
| 18 | BD1-20 "XAP 9" | nc | nc |
| 19 | BD1-19 "XAP 8" | nc | nc |
| 20 | BD1-21 "XAP 13" | nc | nc |
| 21 | nc | nc | nc |
| 22 | BD1-16 "PIO-15" | nc | nc |
| 23 | BD1-15 "PIO-14" | nc | nc |
| 24 | BD1-14 "PIO-13" | nc | nc |
| 25 | BD1-13 "PIO-12" | nc | nc |

E. Discrete Components

Capacitors

| | |
|--------|------------|
| .01 uF | HH-1/HH-14 |
| .2 uF | HH-2/HH-13 |
| .20 uF | HH-3/HH-12 |
| .01 uF | HH-4/HH-11 |
| .01 uF | HH-5/HH-10 |
| .47 uF | HH-7/HH-8 |

Resistors

| Trimmer | | | |
|---------|-----|------|-----|
| | 1 | 2 | 3 |
| A | Y-1 | Y-13 | Z-7 |
| B | VCC | VCC | VCC |
| C | VCC | VCC | VCC |

APPENDIX B ATAC OPERATING INSTRUCTIONS

Power Up

Turn on front panel power then turn on power supplies.

Power Down

'Halt'

'Master Clear'

Power off to supplies, power off to control panel.

Run Program

'Master Clear'

Dial 'IMR'

'AUX REG'

'ENTER' (associated with AUX REG)

'MEMORY'

Set start address +1 in keyboard (Hexadecimal)

'PCP'

'ENTER' (associated with PCP)

'RUN'

Stop a Program

'HALT'

Read Memory (from front panel)

'HALT'

Set desired address in key board

Select 'MAR'

'ENTER' (associated with MAR)

'INC' (increment)

'DEC' (decrement)

Address is displayed above MAR key, data is displayed in red LEDs above MEMORY key.

Use INC or DEC as necessary to arrive at memory location desired.

Write into Memory (from front panel)

'HALT'

Set address desired as described in Read Memory.

Set desired data into keyboard

'ENTER' (associated with MEMORY)

Value in keyboard will be entered into either Memory (MEMORY) or A computer Register (FILE).

Bootstrap Load (paper tape)

'HALT'

'MASTER CLEAR'

'AUX REG'

Dial 'IMR'

'ENTER' (associated with AUX REG)

'MEMORY'

Set 0001 in keyboard

(0001 = Load, 0002 = Verify only)

'RUN'

At end of tape check program status lights (red LEDs
below PCR and MAR pushbuttons)

0000 = Load good

FFFE = Parity error

FFFD = Verify error

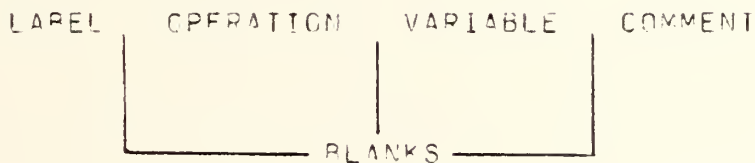
APPENDIX C

ATAC PROGRAM ASSEMBLY

Assembly of a program is divided into five parts; writing, producing absolute deck on IBM 360, conversion of absolute deck into ATAC format, punching paper tape, and loading ATAC.

A. Writing the program.

Programs for the ATAC must be written in the assembly language described in ATAC manuals Volumes One, and Eight. The finished program must be placed on cards for the IBM 360 in the following format:



B. Producing an absolute deck

The first step is to load the assembler on to the IBM 360 from magnetic tape. This is done by executing program A in Appendix E. This transfers the program from tape to disk and saves it for one year. Once the assembler is stored the

following cards placed in the front of a program written following the instructions in I above will produce an absolute deck and a print-out of the program.

```
//ATACASSM JOB (0729,0194,0052),'CCH ATAC ASSEM,',TIME=1
//ASSEM EXEC PGM=APSS,REGION=220K
//STEPLIB DD DSN=S0729.ATAC.0NF,UNIT=3330,
// VOL=SER=DISK02,DISP=SHR
//FT06F001 DD SYSCUT=A
//FT07F001 DD SYSCUT=B
//FT05F001 DD DDNAME=SYSIN
//FT08F001 DD UNIT=SYSDA,SPACE=(CYL,1)
//FT09F001 DD UNIT=SYSDA,SPACE=(CYL,(7,2)),
// DCB=(RECFM=VRS,BLKSIZE=7180,LRECL=92)
//FT10F001 DD UNIT=SYSDA,SPACE=(CYL,(7,2)),
// DCB=(RECFM=VRS,BLKSIZE=4204,LRECL=42)
//FT20F001 DD UNIT=SYSDA,SPACE=(CYL,(7,2)),
// DCB=(RECFM=VRS,BLKSIZE=2004,LRECL=500)
//SYSPPRINT DD SYSCUT=A
//SYSIN DD *
$JOB
$ASSEM
IDT ATAC
```

(place written program here.)

```
END
$BASE
$LOAD P
$END
```

The absolute deck is in the form:

```
0500169c0009adbc3109bc9c3109c8bc7109c09c7109d8b60309bbd932/
/e10100009c015a
05101609f49c0109f6ec0008aee1010a02ed000230e10109f7ed0007ae/
/c1070509201258
```

which must be translated for the ATAC. The memory location of the first word is located in the first four columns.

Columns five and six contain the number of word fields on the card. The assembled program is located in columns 7 - 70. The remaining two columns are parity.

C. Conversion

The absolute deck received from the IBM 360 is loaded into the PDP-11. After the data from the cards is checked, the conversion program (convert 'filename' 'filename') can be executed. (Program C in Appendix F)

D. Punching Paper Tape

This code must then be transferred to the PDP-11 (A) where a paper tape can be punched. Here, the command to punch a tape is:

```
cat 'filename' >/dev/ptp
```

E. Loading the AT10

In order to load a tape the RS232 connector must be connected to the Paper Tape reader and the reader set to 1200 baud. The tape is loaded by following the instructions in Appendix B.

APPENDIX D

SAMPLE ATAC OUTPUT

Operator inputs are underlined.

| Operator Display | *Comments |
|-------------------------|-------------------------|
| EXEC | *Executive echos |
| + <u>JDKDKJFJJJnmun</u> | *entries other than |
| JDKDKJFJJJNMUH | *commands |
| EXEC | |
| + <u>CO</u> | *Entry into CORE |
| CORE | |
| + <u>CH 0F00 0900</u> | *Location 0F00 |
| 0F00 0900 | *changed to 0900 |
| CORE | |
| + <u>DI 0F00</u> | |
| 0F00 0900 | |
| CORE | |
| + <u>CS 0F00</u> | *Locations 0F00 to 0F03 |
| + <u>0256</u> | *changed |
| + <u>0123</u> | |
| + <u>4567</u> | |
| + <u>DO</u> | *completion of change |
| 0F00 0256 | |
| 0F01 0123 | |
| 0F02 4567 | |
| CORE | |
| + <u>DO</u> | *Exit from CORE |
| + <u>WJ</u> | *Entry into Receiver |
| 0=SET-UP | *Control |
| 1=DISPLAY TENTATIVE | |
| 2=DISPLAY CONTROL | |
| 3=DISPLAY RECEIVED | |
| 4=ENTER TENTATIVE | |
| 5=SCAN | |

6=RECEIVE CONTROL

7=DONE

8=REINITIALIZE

RECEIVER CONTROL

+0

FREQ(HZ)

+1240000

DETECT MODE

0=AM

1=FM

2=BFO FIXED

3=BFO VARIABLE

4=ISB

5=USB

6=LSB

7=AM-NL

+0

GAIN MODE

0=HOLD AGC

2=NORMAL AGC

3=MANUAL AGC

+0

IF BANDWIDTH

1=500 HZ

2=2 KHZ

3=4 KHZ

4=8 KHZ

+4

RF GAIN

(PERCENTAGE)

+88

RECEIVER CONTROL

+1

FREQ = 1240000 HZ

GAIN MODE = HOLD AGC

IF BANDWIDTH = 2 KHZ

DETECT MODE = AM

BFO FREQ = 455000 HZ

RF GAIN = 88%

RECEIVER CONTROL

+2

FREQ = 550000 HZ
GAIN MODE = NORMAL AGC
IF BANDWIDTH = 8 KHZ
DETECT MODE = AM
BFO FREQUENCY = 455000 HZ
RF GAIN = 85%

RECEIVER CONTROL

+3

FREQ = 550000 HZ
GAIN MODE = NORMAL AGC
IF BANDWIDTH = 8 KHZ
DETECT MODE = AM
BFO FREQUENCY = 455000 HZ
RF GAIN = 85%
SIGNAL STRENGTH = 66%

RECEIVER CONTROL

+4

RECEIVER CONTROL

+5

SCAN
START FREQ IN HZ

+1000000

END FREQ IN HZ

+1008000

FREQ INCREMENT IN HZ

+1000

SIGNAL STRENGTH %

+67

FREQ = 1001000
GAIN MODE = HOLD AGC
IF BANDWIDTH = 8 KHZ
DETECT MODE = AM
BFO FREQUENCY = 455000 HZ
RF GAIN = 88%
SIGNAL STRENGTH = 72%

RECEIVER CONTROL

+7

EYEC

+

*Exit from
*Receiver Control

APPENDIX E CONVERSION PROGRAMS FOR THE ASSEMBLER

A. This program is run on the IBM-360 to transfer the ATAC assembler from tape ATT-006 to Disk and stores it there for one year.

```
// (GREEN JOB CARD)
//SYSPRINT DD   SYSCUT=A
//SYSUT1 DD    UNIT=SYSDA,SPACE=(TRK,(40),,CONTIG)
//DA1 DD       UNIT=2314,DSN=S0729.ATAC.ONE,
//              SPACE=(TRK,(50,10,10),,CONTIG),
//              DISP=(NEW,KEEP),VOL=SER=SPOOL3
//T1TAPE DD UNIT=(2400,,DFFER),DISP=(NEW,PASS),
//              LABEL=(3,SL,,IN),
//              DCR=(DFN=2,BLKSIZE=800,LRECL=80,RECFM=FB),
//              VOL=SER=ATT006
//SYSIN DD      *
//              COPY PDS=ATT.APSS.LOADLIB,TO=2314=SPOOL3,
//                  FROMDD=T1TAPE,FROM=2400=(ATT006,3),
//                  RENAME=S0729.ATAC.ONE

/*
//BUILD EXEC PGM=IEWL,REGION=150K,
//      PARM='DVLY,XPEF,LET,LIST,SIZE=(256K,20480)'
//SYSPRINT DD   SYSCUT=A
//LIBRARY DD DSN=S0729.ATAC.ONE,UNIT=2314,VOL=SER=SPOOL3,
//          DISP=SHR
//SYSLIB DD DSN=SYS1.FORTLIB,DISP=SHR
//SYSLMOD DD DSN=S0729.ATAC.ONE,
//          UNIT=3330,VOL=SER=DISK02,
//          DISP=(NEW,KEEP),LABEL=PETPD=360,
//          SPACE=(CYL,(5,1,2),RLSE)
//SYSUT1 DD UNIT=SYSDA,SPACE=(TRK,(19,19),,CONTIG),
//          SEP=SYSLMOD
//SYSLIN DD      *
//      INCLUDE LIBRARY(PVHDL)
//      CHANGE MSIM(IHESAPD)
//      INCLUDE LIBRARY(APSSMON)
//      INCLUDE LIBRARY(MSIM4,ASEM5,SIM16A,SIMTR1,SIMIO1,GUL)
//      INCLUDE LIBRARY(XPLMON)
//      OVERLAY A1
//      INSERT MSIMUL,*MSIMUL A,IHENTRY,IHESAP
//      INSERT MINT,IN,OUT
//      INSERT IHEDBN,IHEXTD
```



```

INSERT IHERSM,IHECSM
INSERT IHERSK,IHFIOX,IHEIOP,IHEDIO,IHEDOB
INSERT IHEDIB,IHEDOA,IHEIOB
INSERT IHEIOA,IHEOCL
INSERT IHERSD,IHEBSF
INSERT IHEJXS
INSERT IHEOSD,IHEOST,IHERST
INSERT IHEVPF,IHEDMA,IHEVER
INSERT IHEDNC,IHEVFD,IHEVFA,IHEVPD,IHEVPR,IHEVSC
INSERT IHEVSD,IHEVFE,IHEDCN,IHEUPB
INSERT IHEVFC,IHEVPE,IHEVPG,IHEVOB,IHEVGC
INSERT IHEABN,IHEIOO,IHEIOF,IHEPRT,IHEVQA,IHESPR
INSERT IHEREG,IHEPR,IHESI7
INSERT MISEF
OVERLAY A1
INSERT ASSEM,REWIND,REW72,DSKOUT,CARDIN,DISKIN,ERPRT,PRIADD
INSERT WRDATA,PRICOM,PRINOP,RTITEX,REFTIT,PREF,EPTIT
OVERLAY A1
INSERT PARMRD,PRESTM
OVERLAY A1
INSERT SMLTR,HPMTR,STRISM,TRAGE
INSERT RDCPD,ABNPMT,ARTHER,TRACE,HGRAY,HGRAMI,HGRAMS
INSERT IQINIT,ACTIVE,STMTIM
INSERT DEVDTA,ACT,TIME,INT,RAND,DEADT,DEPUG
OVERLAY A2
INSERT LEVEL,DMAIOI,DMAIOA,DMAIOD,RIOIO
INSERT REMACT,DMA,DMAIM,RIO,RIOIM,RIOINT,INTOLY,DMAINT
INSERT DTRAN,PUTACT,RANDOM
OVERLAY A1
INSERT HGPRNT
OVERLAY A1
INSERT LOADER
OVERLAY A1
INSERT LINK,ENTEXT,SILH
OVERLAY A1
INSERT PLATAC,TOPACK
OVERLAY $OBJECT(REGION)
INSERT OBJECT,INIT,LIB,RCALPH,RCHEX,RCINT
INSERT MDATE
OVERLAY $DUMP(REGION)
INSERT SMDUMP,PAGE
ENTRY MAIN
NAME APSS
/*
//          EXEC PGM=IEBCOPY
//SYSPRINT DD   SYSCUT=A
//SYSUT1  DD   DISP=SHR,UNIT=2314,VOL=SER=SP00L3,
//          DSN=S0729.ATAC.ONE
//SYSUT2 DD   DISP=(NEW,PASS),UNIT=3330,VOL=SER=DISK02,
//          DSN=S0729.ATAC.TWO,

```



```

//          SPACE=(13030,(61,0,14),RLSE),
//          DCB=(RECFM=U,BLKSIZE=13030),
//          ,LABEL=RFIPD=360
//SYSUT3 DD UNIT=SYSDA,SPACE=(TRK,(20,5))
//SYSUT4 DD UNIT=SYSDA,SPACE=(TRK,(20,5))
//SYSIN DD *
COPY OUTDD=SYSUT2,INDD=SYSUT1

```

d. This program converts the IBM-360 absolute deck into correct format for the ATAC.

```

main (argc, *argv)
    int argc;
    char *argv [1];
    {register crctr, index, index;
    int stchr;
    int tcopy [731];
    struct buftr
        {int fides;
        int nleft;
        char *nextp;
        char *buffs [512];
        } bufin, bufot, *pnt1, *pnt2;
    stchr = 020;
    if (argc != 3)
        {printf ("Calling arguments are incorrect#");
        exit (0);
        }
    bufin.fides = open (argv [1], 0);
    if (bufin.fides < 0)
        {printf ("Cannot open %s#", argv [1]);
        exit (0);
        }
    pnt1 = &bufin.fides;
    bufot.fides = creat (argv [2], 0777);
    if (bufot.fides < 0)
        {printf ("Cannot open %s#", argv [2]);
        exit (0);
        }
    pnt2 = &bufot.fides;
    outc (stchr, pnt2);
    while (crctr >= 0 && index <= 72)
        tcopy [index++] = (crctr =getc (pnt1));
    index = - 3;
    index = 0;
    while (index < 4 && index < index)

```



```

       putc (tmpary [jindex++], pnter2);
index = index + 2;
while (cncrtr >= 0)
    {while (jindex < index)
        {if (tmpary [jindex] == '#')
            jindex++;
        else
            putc (tmpary [jindex++], pnter2);
        }
    index = 0;
    while (cncrtr >= 0 && index <= 72)
        tmpary [index++] = (cncrtr =getc (pnter1));
    index -= 3;
    jindex = 0;
}
putc (stchr, pnter2);
fflush (pnter2);
close (bufin.flcse);
close (bufot.flcse);
}

```

C. The following program executes the program above and converts the output into the correct code.

```

atrac $1 $2
if ! -r $2 exit
mv $2 temp2
tr "[0*]" "[040*]" <temp2 >temp1
tr "[1*]" "[001*]" <temp1 >temp2
tr "[2*]" "[002*]" <temp2 >temp1
tr "[3*]" "[043*]" <temp1 >temp2
tr "[4*]" "[004*]" <temp2 >temp1
tr "[5*]" "[045*]" <temp1 >temp2
tr "[6*]" "[046*]" <temp2 >temp1
tr "[7*]" "[007*]" <temp1 >temp2
tr "[8*]" "[010*]" <temp2 >temp1
tr "[9*]" "[051*]" <temp1 >temp2
tr "[a*]" "[052*]" <temp2 >temp1
tr "[b*]" "[013*]" <temp1 >temp2
tr "[c*]" "[054*]" <temp2 >temp1
tr "[d*]" "[015*]" <temp1 >temp2
tr "[e*]" "[016*]" <temp2 >temp1
tr "[f*]" "[057*]" <temp1 >$2
rm temp1 temp2

```


APPENDIX F
ATAC PROGRAM

The following programs are listings of the Main System and Receiver Control programs for the ATAC. The assembly language is to the right of the absolute listing of the first three columns.

TIME: 15:22:49 03/21/77

PAGE

ATAC

| LOC | OBJECT CODE | CARD IMAGE | CARDNUM |
|------|-------------|-------------------|---------|
| 0117 | A446 | IOR R,EU,EL | 55 |
| 0118 | B106 776A | CHP I,EU,0776A | 56 |
| 011A | C102 012A | BRCL EQ,EX2 | 57 |
| 011C | 0000 | NOP | 58 |
| 011D | 0000 | NOP | 59 |
| 011E | 0000 | NOP | 60 |
| 011F | 0000 | NOP | 61 |
| | | | 62 |
| | | | 63 |
| | | | 64 |
| | | | 65 |
| 0120 | BCF0 0136 | LDRM D,0,EXECS,16 | 66 |
| 0122 | ED00 0230 | BAL I,0,OUTPUT | 67 |
| 0124 | E101 014A | LDR I,1,EXCRLF | 68 |
| 0126 | ED00 0230 | BAL I,0,OUTPU | 69 |
| 0128 | C107 0100 | BRC I,7,EXEC | 70 |
| | | | 71 |
| | | | 72 |
| | | | 73 |
| 012A | ED00 0500 | BAL I,RET,WJ | 74 |
| 012C | C107 0100 | BRCL U,EXEC | 75 |
| | | | 76 |
| | | | 77 |
| | | | 78 |
| | | | 79 |
| 012E | 0000 | NOP | 80 |
| 012F | 0000 | NOP | 81 |
| 0130 | 0000 | NOP | 82 |
| 0131 | 0000 | NOP | 83 |
| 0132 | 0000 | NOP | 84 |
| 0133 | 0000 | NOP | 85 |
| 0134 | C107 0100 | BRCL U,EXEC | 86 |
| | | | 87 |
| | | | 88 |
| | | | 89 |
| | | | 90 |
| | | | 91 |
| | | | 92 |
| | | | 93 |
| | | | 94 |
| 0136 | | EXECS DS 20 | 95 |
| 014A | 0001 | EXCRLF DC 1 | 96 |
| 014B | 000A | DC 0000A | 97 |
| 014C | 0000 | DC 0 | 98 |
| 014D | 0003 | DC 3 | 99 |
| 014E | 4558 | DC 04558 | 100 |
| 014F | 4543 | DC 04543 | 101 |
| 0150 | 0000 | DC 0 | 102 |
| | | | 103 |
| | | | 104 |
| | | | 105 |
| | | | 106 |
| | | | 107 |

COMBINE FIRST TWO BYTES OF COMMAND

SEE IF REQUEST FOR WJ

GO CALL WJ

PATCH AREA FOR ANOTHER REQUEST

RESTORE REGISTERS

ECHO INPUT BUFFER

GET ADDRESS OF CR/LF BUFFER

OUTPUT CR/LF

GO TRY AGAIN

GO TO WJ

SAVE AREA FOR REGISTERS

CR/LF

COUNT

EX

EC

NULL

03/21/77

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ATAC

| LOC | OBJECT CODE | CARD | IMAGE | CARDNUM |
|------|-------------|-------|--------|---------|
| 0151 | 9C00 01D6 | KV2 | EQU 12 | 161 |
| 0153 | E101 0228 | KV3 | EQU 13 | 162 |
| 0155 | ED00 0230 | KDATA | EQU 14 | 163 |
| 0157 | E10A FFBF | . | . | 164 |
| 0159 | E109 BFFF | . | . | 165 |
| 015B | 4015 | . | . | 166 |
| 015C | 4016 | . | . | 167 |
| 015D | E101 01D7 | . | . | 168 |
| 015F | E107 8000 | . | . | 169 |
| 0161 | D997 | . | . | 170 |
| | | . | . | 171 |
| | | . | . | 172 |
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| | | . | . | 197 |
| | | . | . | 198 |
| | | . | . | 199 |
| | | . | . | 200 |
| | | . | . | 201 |
| | | . | . | 202 |
| | | . | . | 203 |
| | | . | . | 204 |
| | | . | . | 205 |
| | | . | . | 206 |
| | | . | . | 207 |
| | | . | . | 208 |
| | | . | . | 209 |
| | | . | . | 210 |
| | | . | . | 211 |
| | | . | . | 212 |
| | | . | . | 213 |

VARIABLE 2
 VARIABLE 3
 INPUT DATA FROM KEYBOARD

SAVE RETURN ADDRESS
 GET ADDRESS OF BUFFER TO OUTPUT
 OUTPUT INITIAL BUFFER
 DEVICE ADDRESS FOR INPUT
 DEVICE ADDRESS FOR OUTPUT
 POSITION COUNTER = 1
 MAX POSITION USED = 1
 ADDRESS OF BUFFER TO REGISTER
 RESET CODE
 RESET KEYBOARD

 SET KEYBOARD ENTRY BUFFER TO ALL SPACES

SET COUNTER TO BUFFER LENGTH
 GET BUFFER ADDRESS
 CODE FOR SPACE
 STORE BLANK CODE
 DECREMENT COUNTER
 DO AGAIN IF COUNTER GT 0

RESET CODE
 RESET KEYBOARD

GET DATA
 DATA PRESENT ?
 NO DATA, WAIT

RESET CODE
 RESET KEYBOARD
 GET STATUS
 DATA STILL PRESENT ?
 KEEP TRYING TO CLEAR

 DATA OBTAINED

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PAGE

ATAC

| LOC | OBJECT CODE | CARD IMAGE | CARDNUM |
|------|-------------|----------------|---------|
| 0179 | A10E 007F | AND | 214 |
| 017B | 208E | IS,KDATA,0007F | 215 |
| 017C | C105 0184 | IS,KDATA,008 | 216 |
| 017E | 6FF5 | NE,KMR6 | 217 |
| 017F | C105 0169 | ADD | 218 |
| 0181 | 4015 | IS,KDEC-1 | 219 |
| 0182 | C107 0169 | BRCL | 220 |
| | | NZ,KMR3 | 221 |
| | | LDR | 222 |
| | | IS,KPC,1 | 223 |
| | | U,KMR3 | 224 |
| KMR6 | | BRCL | 225 |
| 0184 | 21CE | IS,KDATA,01C | 226 |
| 0185 | C105 0190 | NE,KMR8 | 227 |
| 0187 | 6015 | ADD | 228 |
| 0188 | B105 0050 | IS,KPC,1 | 229 |
| 018A | C106 0169 | BRCL | 230 |
| 018C | E105 0050 | LE,KMR3 | 231 |
| 018E | C107 01B9 | LDR | 232 |
| | | U,KMR11 | 233 |
| | | BRCL | 234 |
| KMR8 | | BRCL | 235 |
| 0190 | 20DE | IS,KDATA,00D | 236 |
| 0191 | C102 01B9 | EQ,KMR11 | 237 |
| 0193 | 22EE | IS,KDATA,02E | 238 |
| 0194 | C102 01B1 | BRCL | 239 |
| 0196 | 220E | IS,KDATA,020 | 240 |
| 0197 | C102 01B1 | BRCL | 241 |
| | | EQ,KMR10 | 242 |
| 0199 | 230E | IS,KDATA,030 | 243 |
| 019A | C104 01AB | LT,KMR9 | 244 |
| 019C | 239E | IS,KDATA,039 | 245 |
| 019D | C106 01B1 | BRCL | 246 |
| | | LE,KMR10 | 247 |
| 019F | 241E | IS,KDATA,041 | 248 |
| 01A0 | C104 01AB | LT,KMR9 | 249 |
| 01A2 | 25AE | IS,KDATA,05A | 250 |
| 01A3 | C106 01B1 | BRCL | 251 |
| | | LE,KMR10 | 252 |
| 01A5 | 261E | IS,KDATA,061 | 253 |
| 01A6 | C104 01AB | LT,KMR9 | 254 |
| 01A8 | 27AE | IS,KDATA,07A | 255 |
| 01A9 | C106 01B1 | BRCL | 256 |
| | | LE,KMR10 | 257 |
| | | ***** | 258 |
| | | ILLEGAL ENTRY | 259 |
| | | ***** | 260 |
| KMR9 | | ***** | 261 |
| 01AB | E101 022C | LDR | 262 |
| 01AD | ED00 0230 | I,KBUF,KBILL | 263 |
| 01AF | C107 0153 | I,KRET,OUTPUT | 264 |
| | | U,KMR1 | 265 |
| | | BRCL | 266 |

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| LOC | OBJECT CODE | CARD IMAGE | CARDNUM |
|------|-------------|---|---------|
| 024E | 6025 | ADD IS,BO,2 | 426 |
| 024F | 6FFB | ADD IS,RI,-1 | 427 |
| 0250 | C101 0246 | BRCL GT,OUT1 | 428 |
| | | IN THE OUTPUT BUFFER | 429 |
| | | INCREMENT OUTPUT BUFFER POINTER | 430 |
| | | DECREMENT NUMBER OF WORDS TO INPUT | 431 |
| | | DO AGAIN IF NOT FINISHED | 432 |
| | | ***** | 433 |
| | | OUTPUT WORD BUFFER | 434 |
| | | ***** | 435 |
| 0252 | F105 026B | LDR I,BO,BUPOUT | 436 |
| 0254 | 8C6E 1000 | RIN SP,WIN | 437 |
| 0255 | 9E0E 1000 | IF,STAT,01000 | 438 |
| 0257 | C105 0254 | BRCL NE,OUT2 | 439 |
| 0259 | B75F 0000 | LLC DX,OUT2 | 440 |
| 025B | A10F 01FF | AND I,OUT,00FF | 441 |
| 025D | D97F | ROUT WOUT,VOUT | 442 |
| 025E | 6015 | ADD IS,BO,1 | 443 |
| 025F | 6FFA 0254 | ADD IS,BO,-1 | 444 |
| | | BRCL GT,OUT2 | 445 |
| | | GET ADDRESS OF OUTPUT BUFFER | 446 |
| | | GET STATUS | 447 |
| | | COMPARE STATUS | 448 |
| | | WAIT IF NOT READY | 449 |
| | | GET CHARACTER TO OUTPUT | 450 |
| | | CLEAR ALL BUT OUTPUT DATA | 451 |
| | | OUTPUT A CHARACTER | 452 |
| | | INCREMENT BUFFER POINTER | 453 |
| | | DECREMENT WORD COUNTER | 454 |
| | | GO OUTPUT ANOTHER CHARACTER | 455 |
| | | ***** | 456 |
| | | EXIT | 457 |
| | | ***** | 458 |
| 0262 | F20B 026A | LDR D,WI,COUNT | 459 |
| 0264 | C102 0269 | BRCL Z,OUT4 | 460 |
| 0266 | 6D8B | ADD IS,WI,-40 | 461 |
| 0267 | C107 0237 | BRCL U,OUT5 | 462 |
| C269 | BF07 | BRC R,7,RET | 463 |
| | | OUT4 | 464 |
| | | ***** | 465 |
| | | DATA | 466 |
| | | ***** | 467 |
| 026A | | COUNT DS 1 | 468 |
| 026B | | BUFOUT DS 80 | 469 |
| | | SAVE LOCATION FOR EXCESS COUNT | 470 |
| | | OUTPUT BUFFER | 471 |
| | | ***** | 472 |
| | | HEX | 473 |
| | | ROUTINE TO CONVERT HEX TO ASCII | 474 |
| | | CALLING PROCEDURE: | 475 |
| | | DAL I,0,HEXA | 476 |
| | | INPUTS: | 477 |
| | | REG 1 HEX VALUE TO BE CONVERTED (INTEGER) | 478 |
| | | OUTPUTS: | |

CARDNUM

CARD IMAGE

OBJECT CODE

```

. . . REG 2 MOST SIGNIFICANT TWO DIGITS IN ASCII *
. . . REG 3 LEAST SIGNIFICANT TWO DIGITS IN ASCII *
. . . LETTERS ARE OUTPUT IN UPPER CASE *
. . . ROUTINES CALLED: *
. . . NONE *
. . . THIS ROUTINE DOES NOT DISTURB REGISTERS 5 THROUGH 15 *
. . . *****
. . . ESTABLISH EQUATES *****
. . . *****
HIN EQU 1
MSD EQU 2
LSD EQU 3
V1 EQU 4
. . .
. . . *****
. . . CONVERT TO ASCII *****
. . . *****
HEXA STR D,RET,HEXRTN
E014 LDR R,V1,HIN
ADB4 SHS R,V1,12
E000 BAL I,RET,HEXA1
AE74 SHS LL,V1,8
E042 LDR R,MSD,V1
E014 LDR R,V1,HIN
AD74 SHS R,V1,8
A104 AND I,V1,0000F
E000 BAL I,RET,HEXA1
8042 LDR R,MSD,V1
E014 LDR R,V1,HIN
A104 AND I,V1,0000F
AD34 SHS RL,V1,4
E000 BAL I,RET,HEXA1
AE74 SHS LL,V1,8
E043 LDR R,LSD,V1
E014 LDR R,V1,HIN
A104 AND I,V1,OF
E000 BAL I,RET,HEXA1
8043 ADD R,LSD,V1
. . .
. . . *****
. . . EXIT *****
. . .

```

HEX VALUE INPUT
MOST SIGNIFICANT DIGITS TO OUTPUT
LEAST SIGNIFICANT DIGITS TO OUTPUT
VARIABLE

03/21/77

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PAGE

ATAC

CARDNUM

CARD IMAGE

OBJECT CODE

LOC

02D8 E200 02E1 LDR D,RET,HEXRTN GET RETURN ADDRESS
02DA BF07 BRC R,7,RET RETURN

* CONVERT ONE DIGIT
* *****

02DB 20A4 CMP IS,V1,00A COMPARE WITH HEX 'A'
02DC C104 BRCL LI,HEXA2 BRANCH IF LESS
02DE 6074 ADD IS,V1,7 ADD 7 TO VALUE
02DF 6304 ADD IS,V1,030 ADD HEX '30'
02E0 BF07 BRC R,7,RET GO BACK TO WHERE CALLED FROM

* DATA
* ***

02E1 HEXRTN DS 1 SAVE LOCATION FOR RETURN ADDRESS

* AHEX

ROUTINE TO CONVERT FOUR (4) OR LESS DIGITS IN ASCII
CODE TO A TRUE HEX VALUE FOR THE MACHINE. SIGN OF THE
VALUE MUST BE HANDLED BY THE CALLING ROUTINE.

CALLING PROCEDURE:
BAL I,0,AHEX

INPUTS:
REG 1 ADDRESS OF FIRST CONSECUTIVE LOCATION IN
CORE WHERE THE ASCII CHARACTERS ARE LOCATED
(ONE CHARACTER PER CORE LOCATION)

OUTPUT:
REG 2 HEX VALUE

ROUTINES CALLED:
NONE

REGISTERS 8 THROUGH 16 ARE PRESERVED

* ESTABLISH EQUATES
* *****

AHADD EQU 1 ADDRESS OF ASCII CHARACTERS
AHOUT EQU 2 OUTPUT HEX VALUE
AHV1 EQU 5 VARIABLE 1

ATAC

| LOC | OBJECT CODE | CARD IMAGE | TIME: 15:22:49 | 03/21/77 | PAGE | CARDNUM |
|------|-------------|--|--|----------|------|--|
| 02F8 | 9C02 03D3 | CV3 CHAX CINDX CFLG CF2 CZERO CSPACE CV1 CV2 CMD CF1 | VARIABLE 3 MAX NO. CHARACTERS INPUT BY KFYMR PATH FLAG BLANK FLAG FIELD2 CONSTANT 0 FOR SPACE ASCII CODE VARIABLE 1 VARIABLE 2 COMMAND FIELD 1 | | | 691 692 693 694 695 696 697 698 699 700 701 702 703 704 705 706 707 708 709 710 |
| 02FA | 400C | CORE | SAVE REGISTER 2 | | | 711 |
| 02FB | 9C0C 0412 | STR | GET A ZERO | | | 712 |
| 02FD | E101 0405 | STR | INITIALIZE SEQUENTIAL CHANGE FLAG | | | 713 |
| 02FF | E100 0230 | LDR | GET BUFFER ADDRESS FOR TITLE | | | 714 |
| 0301 | E100 0151 | EAL | OUTPUT TITLE | | | 715 |
| 0303 | E10B 2020 | BAL | GET KEYBOARD COMMAND | | | 716 |
| 0305 | 400A | LDR | SET UP SPACE CODE | | | 717 |
| 0306 | 400E | LDR | SET UP ZERO CONSTANT | | | 718 |
| 0307 | 400F | LDR | ZERO COMMAND REGISTER | | | 719 |
| 0308 | 4009 | LDR | ZERO FIELD 1 | | | 720 |
| 0309 | 4008 | LDR | ZERO FIELD 2 | | | 721 |
| 030A | 4007 | LDR | ZERO FLAG | | | 722 |
| | | | ZERO PATH FLAG | | | 723 |
| | | | | | | 724 |
| | | | | | | 725 |
| | | | | | | 726 |
| | | | | | | 727 |
| 030B | 51A6 | LDR | GET NO. OF ENTRIES IN BUFFER | | | 728 |
| 030C | 4008 | LDR | SET FLAG FOR READY | | | 729 |
| 030D | 6011 | ADD | INCREMENT BUFFER ADDRESS | | | 730 |
| 030E | 6FF6 | ADD | DECREMENT CHARACTER COUNTER | | | 731 |
| 030F | C106 | BRCL | DONE | | | 732 |
| 0311 | 51AC | LDR | GET A CHARACTER FROM THE BUFFER | | | 733 |
| 0312 | 220C | CMP | COMPARE WITH SPACE | | | 734 |
| 0313 | C102 | BRCL | KEEP GOING IF SPACE | | | 735 |
| 0315 | 2008 | CMP | SEE IF IN MIDDLE OF ENTRY | | | 736 |
| 0316 | C105 | BRCL | IN ENTRY, KEEP LOOKING | | | 737 |
| 0318 | E370 | LDR | GET PROCESSING ADDRESS | | | 738 |
| 031A | BF07 | BRC | PROCESS ACCORDING TO FIELD FOUND | | | 739 |
| | | | | | | 740 |
| | | | | | | 741 |
| | | | | | | 742 |
| | | | | | | 743 |

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ATAC

CARDNUM

CARD IMAGE

OBJECT CODE

CFLD

C4

C5

C6

C7

C8

C9

C10

C11

C12

C13

C14

C15

C16

C17

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C361

C362

CARDNUM

CARD IMAGE

OBJECT CODE

LOC

| | | | | | |
|------|-----------|-----|-------------------------|-----------------------------------|-----|
| 034A | C102 039C | . | BRCL EQ,C12 | GO PROCESS 'CHANGE' | 797 |
| 034C | B10C 6373 | . | CMP I,CV1,06373 | COMPARE WITH 'CS' | 798 |
| 034E | C102 03A0 | . | BRCL EQ,C20 | GO PROCESS CHANGE SEQUENTIAL | 799 |
| 0350 | C107 02FD | . | BRCL U,C1 | INVALID COMMAND, GO TRY AGAIN | 800 |
| | | . | | | 801 |
| | | . | | | 802 |
| | | . | | | 803 |
| | | . | | | 804 |
| | | . | | | 805 |
| | | . | | | 806 |
| | | . | | | 807 |
| | | . | | | 808 |
| | | . | | | 809 |
| | | . | | | 810 |
| 0352 | E200 03D3 | C9 | LDR D,0,CSAVE | GET ADDRESS OF CALLING ROUTINE | 811 |
| 0354 | C107 0151 | . | BRC I,7,KEYMR | EXIT THROUGH KEYMR | 812 |
| | | . | | | 813 |
| | | . | | | 814 |
| | | . | | | 815 |
| | | . | | | 816 |
| | | . | | | 817 |
| | | . | | | 818 |
| | | . | | | 819 |
| | | . | | | 820 |
| 0356 | A10F FFF8 | C10 | AND I,CF1,OFFP8 | CLEAR LAST THREE BITS | 821 |
| 0358 | E0F5 | . | LDR R,CV3,CF1 | SET UP ADDRESS TO DUMP | 822 |
| 0359 | 40A7 | . | LDR IS,CINDX,10 | SET UP LINE COUNTER | 823 |
| | | . | | | 824 |
| | | . | | | 825 |
| | | . | | | 826 |
| 035A | E10C 03D5 | C13 | LDR I,CV1,CBUFF | SET UP OUTPUT BUFFER ADDRESS | 827 |
| 035C | E0AD | . | LDR R,CV2,CZERO | SET UP ROW COUNTER | 828 |
| 035D | E051 | . | LDR R,CADD,CV3 | SET UP INPUT FOR HEXA | 829 |
| 035E | E000 | . | BAL I,RET,HEXA | CONVERT ADDRESS TO ASCII | 830 |
| 0360 | 1AC2 | . | STR RX,CA1,CV1,CZERO | PUT MSD OF ADD IN OUTPUT BUFFER | 831 |
| 0361 | 601C | . | ADD IS,CV1,1 | INCREMENT BUFFER ADDRESS | 832 |
| 0362 | 1AC3 | . | STR RX,CA2,CV1,CZERO | PUT LSD OF ADD IN OUTPUT BUFFER | 833 |
| 0363 | 601C | . | ADD IS,CV1,1 | INCREMENT BUFFER ADDRESS | 834 |
| 0364 | 1ACB | . | STR RX,CSPACE,CV1,CZERO | PUT IN SPACES | 835 |
| 0365 | 601C | . | ADD IS,CV1,1 | INCREMENT BUFFER ADDRESS | 836 |
| 0366 | 1ACB | . | STR RX,CSPACE,CV1,CZERO | PUT IN SPACES | 837 |
| 0367 | 5A51 | . | ADD IS,CV1,1 | INCREMENT BUFFER ADDRESS | 838 |
| 0368 | 6015 | . | LDR RX,CADD,CV3,CZERO | GET VALUE TO CONVERT | 839 |
| 0369 | E000 | . | ADD IS,CV3,1 | INCREMENT ADD OF VALUE TO OUTPUT | 840 |
| 036A | 1AC2 | . | BAL I,RET,HEXA | CONVERT VALUE | 841 |
| 036C | 1AC2 | . | STR RX,CA1,CV1,CZERO | PUT MSD OF VALUE IN OUTPUT BUFFER | 842 |
| 036D | 601C | . | ADD IS,CV1,1 | INCREMENT BUFFER ADDRESS | 843 |
| 036E | 1AC3 | . | STR RX,CA2,CV1,CZERO | PUT LSD OF VALUE IN OUTPUT BUFFER | 844 |
| 036F | 601D | . | ADD IS,CV2,1 | INCREMENT ROW COUNTER | 845 |
| 0370 | 208D | . | CHP IS,CV2,8 | COMPARE WITH LAST VALUE | 846 |
| 0371 | C102 0375 | . | BRCL EQ,C15 | BRANCH IF EQUAL | 847 |
| 0373 | C107 0365 | . | BRCL U,C14 | GO DO ANOTHER VALUE | 848 |
| 0375 | E101 03D5 | C15 | LDR I,CADD,CBUFF | GET BUFFER POINTER | 849 |

| LOC | OBJECT CODE | CARD IMAGE | CARDNUM |
|------|-------------|------------|---------|
| 0377 | 6FF1 | ADD | 850 |
| 0378 | 9CF0 | STRM | 851 |
| 0379 | ED00 | BAL | 852 |
| 037C | BCF0 | LDRM | 853 |
| 037E | 6FF7 | ADD | 854 |
| 037F | C105 | BRC | 855 |
| 0381 | C107 | BRC | 856 |
| . | . | . | 857 |
| . | . | . | 858 |
| . | . | . | 859 |
| . | . | . | 860 |
| . | . | . | 861 |
| . | . | . | 862 |
| . | . | . | 863 |
| . | . | . | 864 |
| C11 | E0F1 | LDR | 865 |
| 0383 | ED00 | BAL | 866 |
| 0384 | 9C02 | STR | 867 |
| 0386 | 9C03 | STR | 868 |
| 0388 | E3F1 | LDR | 869 |
| 038A | E101 | BAL | 870 |
| 038C | ED00 | STR | 871 |
| 038E | 9C02 | STR | 872 |
| 0390 | 9C03 | STR | 873 |
| 0392 | E101 | LDR | 874 |
| 0394 | ED00 | BAL | 875 |
| 0396 | E20F | LDR | 876 |
| 0398 | C105 | BRC | 877 |
| 039A | C107 | BRC | 878 |
| . | . | . | 879 |
| . | . | . | 880 |
| . | . | . | 881 |
| . | . | . | 882 |
| . | . | . | 883 |
| . | . | . | 884 |
| C12 | 9BF9 | STR | 885 |
| 039C | C107 | BRC | 886 |
| 039E | C107 | BRC | 887 |
| . | . | . | 888 |
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| . | . | . | 891 |

CARDNUM

CARD IMAGE

OBJECT CODE

LOC

| | | | | | |
|------|------|------|------|-------------------|-----|
| 03A0 | 9C0F | 0410 | STR | D,CF1,CSTRT | 892 |
| 03A2 | 9C0F | 0411 | STR | D,CF1,CSTOR | 893 |
| 03A4 | 401C | 0412 | LDR | IS,CV1,1 | 894 |
| 03A5 | 9C0C | 0412 | STR | D,CV1,CSFLG | 895 |
| 03A7 | ED00 | 0151 | BAL | I,GET,KEYMR | 896 |
| 03A9 | 400A | | LDR | IS,CZERO,0 | 897 |
| 03AA | 6011 | | ADD | IS,CADD,1 | 898 |
| 03AB | 4015 | | LDR | IS,CV3,1 | 899 |
| 03AC | 51AC | | LDR | RX,CV1,CZERO,CADD | 900 |
| 03AD | 515D | | LDR | RX,CV2,CV3,CADD | 901 |
| 03AE | AE7C | | LDR | LL,CV1,8 | 902 |
| 03AF | AE7D | | SHS | LL,CV2,8 | 903 |
| 03B0 | AD7D | | SHS | RL,CV2,8 | 904 |
| 03B1 | 80DC | | ADD | R,CV1,CV2 | 905 |
| 03B2 | B10C | | CMP | I,CV1,0646P | 906 |
| 03B4 | C102 | 646F | BRCL | E,C22,1 | 907 |
| 03B6 | E200 | 03C1 | BAL | I,GET,AHXX | 908 |
| 03B8 | E20F | 0411 | LDR | D,CF1,CSTOR | 909 |
| 03BA | 9BF2 | 0000 | STR | DX,CA1,0,CF1 | 910 |
| 03BC | 601F | | ADD | IS,CF1,1 | 911 |
| 03BD | 9C0F | 0411 | STR | D,CF1,CSTOR | 912 |
| 03BF | C107 | 03A7 | BRCL | U,C21,CSTRT | 913 |
| 03C1 | E20F | 0410 | LDR | D,CF1,CSTRT | 914 |
| 03C3 | C107 | 03B3 | BRCL | U,C11,CSTRT | 915 |
| 03C5 | E20C | 0410 | LDR | D,CV1,CSTRT | 916 |
| 03C7 | 601C | | ADD | IS,CV1,1 | 917 |
| 03C8 | 9C0C | 0410 | STR | D,CV1,CSTRT | 918 |
| 03CA | B20A | 0411 | CMP | D,CV1,CSTOR | 919 |
| 03CC | C104 | 03C1 | BRCL | LT,C22,1 | 920 |
| 03CE | 400C | | LDR | IS,CV1,0 | 921 |
| 03CF | 9C0C | 0412 | STR | D,CV1,CSFLG | 922 |
| 03D1 | C107 | 02FD | BRCL | U,C1 | 923 |
| 03D3 | | | | | 924 |
| 03D4 | 001C | | | | 925 |
| 03D5 | 000A | | | | 926 |
| 03F0 | 0D0A | | | | 927 |
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SAVE START OF CORE CHANGE
 SAVE ADDRESS AT WHICH TO STORE
 SET UP A CONSTANT 1
 SET FLAG FOR SEQUENTIAL CHANGE
 GET VALUE FOR CORE
 SET UP ZERO REGISTER
 INCREMENT BUFFER POINTER
 SET UP A CONSTANT 1
 GET FIRST CHARACTER ENTERED
 GET SECOND CHARACTER ENTERED
 LEFT JUSTIFY FIRST CHARACTER
 CLEAR UPPER 8 BITS OF SECOND CHAR
 RIGHT JUSTIFY SECOND CHARACTER
 COMBINE BOTH CHARACTERS
 COMPARE WITH 'DO'
 GET OUT IF DONE
 CONVERT ENTRY TO HEX
 GET ADDRESS FOR STORING
 PUT VALUE IN CORE
 INCREMENT STORE ADDRESS
 SAVE NEW STORE ADDRESS
 GO GET ANOTHER ENTRY
 GET ADDRESS OF FIRST CORE CHANGED
 GO DISPLAY LOCATION
 GET ADDRESS DISPLAYED
 INCREMENT ADDRESS
 SAVE AS NEW DISPLAY ADDRESS
 COMPARE WITH LAST ADDRESS+1
 DO AGAIN IF NOT FINISHED
 GET A ZERO SEQUENTIAL FLAG
 CLEAR SEQUENTIAL COMMAND
 GO GET ANOTHER COMMAND

ADDRESS OF ROUTINE CALLING CORE

COUNT OF VALUES IN BUFFER TO OUTPUT
 OUTPUT BUFFER
 CR/LF

 DATA AND BUFFERS

CSAVE DS 1
 CBCNT DC 28
 CBUFF DS 27
 DC 00D0A

03/21/77

TIME: 15:22:49

ATAC

| LOC | OBJECT CODE | CARD IMAGE | REG SAVE AREA FOR INTERNAL USE | CARDNUM |
|------|-------------|----------------|------------------------------------|---------|
| 03F1 | | CS1 DS 20 | | 945 |
| 0405 | 0003 | CTITLE | | 946 |
| 0406 | 0D0A | DC | COUNT | 947 |
| 0407 | 434F | DC | CR/LF | 948 |
| 0408 | 5245 | DC | CO | 949 |
| | | | RE | 950 |
| 0409 | 0006 | CBUD | COUNT | 951 |
| 040A | | CBUD1 DS 6 | 2 LOCATIONS FOR ADDRESS | 952 |
| 040C | 2020 | DC | BLANKS | 953 |
| 040D | | DS 2 | 2 LOCATIONS FOR CONTENTS | 954 |
| 040F | 0D0A | CBUD2 DS 02020 | CR/LF | 955 |
| | | DC | | 956 |
| 0410 | 0000 | CSRT | START ADDRESS FOR SEQUENTIAL CORE | 957 |
| 0411 | 0000 | CSTOR DC 0 | CURRENT ADDRESS FOR STORING IN SEQ | 958 |
| 0412 | 0000 | CSFLG DC 0 | FLAG FOR SEQUENTIAL CHANGE | 959 |
| | | .. | | 960 |
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| | | | | 966 |
| | | END | | |

ATAC

| LOC | OBJECT CODE | CARD | IMAGE | CARDNUM |
|------|-------------|------|---|---------|
| 0539 | C107 0531 | BRCL | U, WJ10 | 108 |
| 053B | 9C03 09D0 | STR | D, WJ10, WJ1FL | 109 |
| 053D | 9C04 09D1 | STR | D, WJ10, WJ1FU | 110 |
| | | | ILLEGAL FREQUENCY | 111 |
| | | | STORE TENTATIVE FREQUENCY {LOWER} | 112 |
| | | | STORE TENTATIVE FREQUENCY {UPPER} | 113 |
| 053F | E101 0A89 | LDR | I, WJ11, WJ1DMB | 114 |
| 0541 | E100 07AE | BAL | I, RET, WJ10 | 115 |
| 0543 | C107 053F | BRCL | U, WJ11 | 116 |
| 0545 | 2073 | CHP | IS, WJ13, 7 | 117 |
| 0546 | C101 053P | BRCL | GT, WJ11 | 118 |
| 0548 | 9C03 09D2 | STR | D, WJ13, WJ1TDM | 119 |
| 054A | E101 0004 | LDR | I, WJ11, 04 | 120 |
| 054C | 5C01 09D4 | STR | D, WJ11, WJ1TBU | 121 |
| 054E | E101 5500 | LDR | I, WJ11, 05500 | 122 |
| 0550 | 9C01 09D3 | STR | D, WJ11, WJ1TBL | 123 |
| 0552 | 2033 | CHP | IS, WJ13, 3 | 124 |
| 0553 | C105 056E | BRCL | NE, WJ13 | 125 |
| | | | NOT VARIABLE BFO, USE DEFAULT FREQUE | 126 |
| | | | OUTPUT TITLE AND GET KEYBOARD ENTRY FOR BFO FREQUENCY | 127 |
| 0555 | E101 0AB3 | LDR | I, WJ11, WJ1BFB | 128 |
| 0557 | E100 07AE | BAL | I, RET, WJ10 | 129 |
| 0559 | C107 0555 | BRCL | U, WJ11 | 130 |
| 055B | 2062 0555 | CHP | IS, WJ12, 6 | 131 |
| 055C | C105 0555 | BRCL | NE, WJ12 | 132 |
| 055E | E833 | SHD | RL, WJ13, 4 | 133 |
| 055F | 2044 | CHP | IS, WJ14, 04 | 134 |
| 0560 | C105 0555 | BRCL | NE, WJ12 | 135 |
| 0562 | B103 4500 | CHP | I, WJ13, 04500 | 136 |
| 0564 | C104 0555 | BRCL | LF, WJ12 | 137 |
| 0566 | B103 6500 | CHP | I, WJ13, 06500 | 138 |
| 0568 | C101 0555 | BRCL | GF, WJ12 | 139 |
| 056A | 9C03 09D3 | STR | D, WJ13, WJ1TBL | 140 |
| 056C | 9C04 09D4 | STR | D, WJ14, WJ1TBU | 141 |
| | | | SAVE TENTATIVE BFO FREQUENCY {LOWER} | 142 |
| | | | SAVE TENTATIVE BFO FREQUENCY {UPPER} | 143 |
| | | | OUTPUT TITLE AND GET KEYBOARD ENTRY FOR GAIN MODE | 144 |
| 056E | E101 0A53 | LDR | I, WJ11, WJ1GMB | 145 |
| 0570 | E100 07AE | BAL | I, RET, WJ10 | 146 |
| 0572 | C107 056E | BRCL | U, WJ13 | 147 |
| 0574 | 2033 | CHP | IS, WJ13, 3 | 148 |
| 0575 | C101 056E | BRCL | GT, WJ11 | 149 |
| 0577 | 2013 | CHP | IS, WJ13, 1 | 150 |
| 0578 | C102 056E | BRCL | EQ, WJ13 | 151 |
| 057A | E201 09D2 | LDR | D, WJ11, WJ1TDM | 152 |
| 057C | 2041 | CHP | IS, WJ11, 4 | 153 |
| 057D | C105 0582 | BRCL | NE, WJ13 | 154 |
| 057F | 2003 | CHP | IS, WJ13, 0 | 155 |
| 0580 | C102 056E | BRCL | EQ, WJ13 | 156 |
| C582 | 9C03 09D5 | STR | D, WJ13, WJ1TGM | 157 |
| | | | MAKE SURE GAIN MODE NOT HOLD AGC | 158 |
| | | | SAVE TENTATIVE GAIN MODE | 159 |
| | | | OUTPUT TITLE AND GET KEYBOARD ENTRY FOR IF BANDWIDTH | 160 |

ATAC
LOC OBJFCT CODE CARD IMAGE

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0584 E201 09D2      .      LDR      D,WJV1,WJTDH      GET TENTATIVE DETECT MODE
0586 E204 058F      .      CMP      IS,WJV1,4      COMPARE WITH IS
0587 C104 058F      .      BRCL    LT,WJV14      NOT SIDE BAND-BRANCH
0589 C061 058F      .      CMP      IS,WJV1,6      COMPARE WITH USE
058A C101 058F      .      BRCL    GT,WJV14      NOT SIDE BAND-BRANCH
058C C043 058F      .      LDR      IS,WJV3,4      LOAD DEFAULT IF BANDWIDTH
058D C107 059B      .      BRCL    U,WJV14,1      SKIP IF BANDWIDTH
058E E101 0A6D      .      LDR      I,WJV1,WJIFB      GET ADDRESS OF IF BANDWIDTH TITLE
0591 ED00 07AE      .      BAL     I,RET,WJ90      OUTPUT TITLE AND GET ENTRY
0593 C107 058F      .      BRCL    U,WJV14      ILLEGAL ENTRY
0595 C043 058F      .      CMP      IS,WJV3,4      COMPARE WITH MAX VALUE
0596 C101 058F      .      BRCL    GT,WJV14      ILLEGAL ENTRY
0598 C013 058F      .      CMP      IS,WJV3,1      COMPARE WITH MIN VALUE
0599 C104 058F      .      BRCL    LT,WJV14      ILLEGAL ENTRY
059B 9C03 09D7      .      STR     D,WJV3,WJTIF      SAVE TENTATIVE IF BANDWIDTH

: OUTPUT TITLE AND GET KEYBOARD ENTRY FOR RF GAIN
WJ15  LDR      I,WJV1,WJRGB      GET ADDRESS OF RF GAIN TITLE
      UAL     I,RET,WJ90      OUTPUT TITLE AND GET ENTRY
      CMP     IS,WJV2,3      ILLEGAL ENTRY
      CMP     I,WJV3,0100     ILLEGAL ENTRY
      STR     D,WJV3,WJTRFG    SAVE TENTATIVE RF GAIN

      BRCL    U,WJ02          GO GET ANOTHER COMMAND

:
:
:
: ***** CONTROL WORD (COMMAND=1) *****
: ***** DISPLAY TENTATIVE *****
WJ20  LDR      D,WJV2,WJTFL      GET UPPER OF TENTATIVE FREQUENCY
      LDR     I,WJV1,WJ92      GET LOWER OF TENTATIVE FREQUENCY
      BAL     D,WJV1,WJTGH      PUT FREQUENCY IN BUFFER AND DISPLAY
      LDR     D,WJV2,WJTIF      GET TENTATIVE GAIN MODE
      BAL     I,RET,WJ93      GET TENTATIVE IF BANDWIDTH
      LDR     D,WJV1,WJTDH      OUTPUT GAIN MODE AND IF BANDWIDTH
      LDR     D,WJV2,WJTBL      GET TENTATIVE DETECT MODE
      BAL     I,RET,WJ94      GET TENTATIVE BFO FREQUENCY (LOWER)
      LDR     I,WJV1,WJTRFG     OUTPUT DETECT MODE AND BFO FREQUENCY
      BAL     D,WJV1,WJ95      GET TENTATIVE RF GAIN
      BRCL    U,WJ02          DONE
05AE E202 09D1      .      LDR      D,WJV2,WJTFL      GET UPPER OF TENTATIVE FREQUENCY
05E0 E201 09D0      .      LDR     I,WJV1,WJ92      GET LOWER OF TENTATIVE FREQUENCY
05E2 ED00 07D9      .      BAL     D,WJV1,WJTGH      PUT FREQUENCY IN BUFFER AND DISPLAY
05E4 E201 09D5      .      LDR     D,WJV2,WJTIF      GET TENTATIVE GAIN MODE
05E6 E202 09D7      .      BAL     I,RET,WJ93      GET TENTATIVE IF BANDWIDTH
05E8 ED00 0801      .      LDR     D,WJV1,WJTDH      OUTPUT GAIN MODE AND IF BANDWIDTH
05EA E201 09D2      .      LDR     D,WJV2,WJTBL      GET TENTATIVE DETECT MODE
05EC E202 09D3      .      BAL     I,RET,WJ94      GET TENTATIVE BFO FREQUENCY (LOWER)
05EE E200 0820      .      LDR     I,WJV1,WJTRFG     OUTPUT DETECT MODE AND BFO FREQUENCY
05C0 E201 09D6      .      LDR     D,WJV1,WJ95      GET TENTATIVE RF GAIN
05C2 ED00 084A      .      BRCL    U,WJ02          DONE
05C4 C107 0519      .

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| LOC | OBJECT CODE | CARD IMAGE | CARDNUM |
|------|-------------|--|---------|
| 05C6 | E202 09D9 | ***** : DISPLAY CONTROL WORD (COMMAND=2) : ***** | 214 |
| 05C8 | E201 09D8 | ***** : ***** | 215 |
| 05CA | E200 07D9 | ***** : ***** | 216 |
| 05CC | E201 09D0 | ***** : ***** | 217 |
| 05CE | E202 09DF | ***** : ***** | 218 |
| 05CF | E200 0801 | ***** : ***** | 219 |
| 05D2 | E201 09DA | ***** : ***** | 220 |
| 05D4 | E202 09DB | ***** : ***** | 221 |
| 05D6 | E200 0820 | ***** : ***** | 222 |
| 05D8 | E201 09DE | ***** : ***** | 223 |
| 05DA | E200 084A | ***** : ***** | 224 |
| 05DC | C107 0519 | ***** : ***** | 225 |
| | | ***** : ***** | 226 |
| | | ***** : ***** | 227 |
| | | ***** : ***** | 228 |
| | | ***** : ***** | 229 |
| | | ***** : ***** | 230 |
| | | ***** : ***** | 231 |
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| | | ***** : ***** | 233 |
| | | ***** : ***** | 234 |
| | | ***** : ***** | 235 |
| | | ***** : ***** | 236 |
| | | ***** : ***** | 237 |
| | | ***** : ***** | 238 |
| 05DE | E202 09E1 | ***** : ***** | 239 |
| 05E0 | E201 09E0 | ***** : ***** | 240 |
| 05E2 | E200 07D9 | ***** : ***** | 241 |
| 05E4 | E201 09E5 | ***** : ***** | 242 |
| 05E6 | E202 09E7 | ***** : ***** | 243 |
| 05E8 | E200 0801 | ***** : ***** | 244 |
| 05EA | E201 09E2 | ***** : ***** | 245 |
| 05EC | E202 09E3 | ***** : ***** | 246 |
| 05EE | E200 0820 | ***** : ***** | 247 |
| 05F0 | E201 09E6 | ***** : ***** | 248 |
| 05F2 | E200 084A | ***** : ***** | 249 |
| 05F4 | E201 09E9 | ***** : ***** | 250 |
| 05F6 | 4002 | ***** : ***** | 251 |
| 05F7 | P871 | ***** : ***** | 252 |
| 05F8 | A502 2030 | ***** : ***** | 253 |
| 05FA | 9C02 0022 | ***** : ***** | 254 |
| 05FC | 4002 | ***** : ***** | 255 |
| 05FD | F831 | ***** : ***** | 256 |
| 05FE | A832 | ***** : ***** | 257 |
| 05FF | F831 | ***** : ***** | 258 |
| 0600 | A502 3030 | ***** : ***** | 259 |
| 0602 | 9C02 0023 | ***** : ***** | 260 |
| 0604 | E101 0018 | ***** : ***** | 261 |
| 0606 | E200 0230 | ***** : ***** | 262 |
| 0608 | C107 0519 | ***** : ***** | 263 |
| 060A | | ***** : ***** | 264 |
| | | ***** : ***** | 265 |
| | | ***** : ***** | 266 |

GET UPPER OF CONTROL FREQUENCY
GET LOWER OF CONTROL FREQUENCY
OUTPUT FREQUENCY
GET CONTROL GAIN MODE
GET CONTROL IF BANDWIDTH
OUTPUT GAIN MODE AND IF BANDWIDTH
GET CONTROL DETECT MODE
GET CONTROL BFO FREQUENCY (LOWER)
OUTPUT DETECT MODE AND BFO FREQUENCY
GET CONTROL RF GAIN
OUTPUT RF GAIN
DONE

GET UPPER OF RECEIVED FREQUENCY
GET LOWER OF RECEIVED FREQUENCY
OUTPUT FREQUENCY
GET RECEIVED IF BANDWIDTH
OUTPUT GAIN MODE AND IF BANDWIDTH
GET RECEIVED DETECT MODE
GET RECEIVED BFO FREQUENCY (LOWER)
OUTPUT DETECT MODE AND BFO FREQUENCY
GET RECEIVED RF GAIN
OUTPUT RF GAIN
GET RECEIVED SIGNAL STRENGTH
CLEAR REGISTER
POSITION 1ST DIGIT
CONVERT TO ASCII
STORE IN OUTPUT BUFFER
CLEAR REGISTER
POSITION LAST 2 DIGITS

CONVERT TO ASCII
STORE IN OUTPUT BUFFER
GET ADDRESS OF BUFFER TO OUTPUT
OUTPUT BUFFER
DONE
SPACE FOR PATCHES

03/18/77

TIME: 16:43:42

ATAC

| LOC | OBJECT CODE | CARD | IMAGE | CARDNUM |
|------|-------------|------|----------------------|---------|
| 0658 | 9C01 09F0 | STR | D,WJV1,WJSCNT | 320 |
| 065A | E101 0B97 | LDR | I,WJV1,WJSCAN | 321 |
| 065C | ED00 0230 | BAL | I,RET,OUTPUT | 322 |
| 065E | | DS | 16 SPACE FOR PATCHES | 323 |
| | | | | 324 |
| | | | | 325 |
| 0668 | E101 0B9C | IDR | I,WJV1,WJSFS | 326 |
| 066A | ED00 07AE | BAL | I,RET,WJ90 | 327 |
| 066C | C107 0668 | BRCL | U,WJ601 | 328 |
| 066E | ED00 07CC | EAL | I,RET,WJ91 | 329 |
| 0670 | C107 0668 | BRCL | U,WJ601 | 330 |
| 0672 | 9C03 09ED | STR | D,WJV3,WJSFLL | 331 |
| 0674 | 9C04 09EC | STR | D,WJV4,WJSFLO | 332 |
| 0676 | | DS | 16 SPACE FOR PATCHES | 333 |
| | | | | 334 |
| | | | | 335 |
| | | | | 336 |
| | | | | 337 |
| 0680 | E101 0BA7 | LDR | I,WJV1,WJSFE | 338 |
| 0682 | ED00 07AE | BAL | I,RET,WJ90 | 339 |
| 0684 | C107 0680 | BRCL | U,WJ602 | 340 |
| 0686 | ED00 07CC | EAL | I,RET,WJ91 | 341 |
| 0688 | C107 0680 | BRCL | U,WJ602 | 342 |
| 068A | 9C03 09EB | STR | D,WJV3,WJSFHL | 343 |
| 068C | 9C04 09EA | STR | D,WJV4,WJSF00 | 344 |
| 068E | | DS | 16 SPACE FOR PATCHES | 345 |
| | | | | 346 |
| | | | | 347 |
| | | | | 348 |
| 0698 | E205 09EC | LDR | D,WJV5,WJSFLO | 349 |
| 069A | E054 | CHP | R,WJV4,WJV5 | 350 |
| 069B | C104 0668 | BRCL | LT,WJ601 | 351 |
| 069D | C101 06AE | BRCL | GT,WJ603 | 352 |
| 069F | E205 09ED | LDR | D,WJV5,WJSFLL | 353 |
| 06A1 | B053 | CHP | R,WJV3,WJV5 | 354 |
| 06A2 | C106 0668 | BRCL | LE,WJ601 | 355 |
| 06A4 | | DS | 16 SPACE FOR PATCHES | 356 |
| | | | | 357 |
| | | | | 358 |
| | | | | 359 |
| 06AE | E101 0BB1 | LDR | I,WJV1,WJSFI | 360 |
| 06E0 | ED00 07AE | BAL | I,RET,WJ90 | 361 |
| 06E2 | C107 06AE | BRCL | U,WJ603 | 362 |
| 06E4 | E833 | SHD | RF,WJV3,4 | 363 |
| 06E5 | 2042 | CHP | IS,WJV2,4 | 364 |
| 06E6 | C101 06AE | BRCL | GT,WJ603 | 365 |
| 06E8 | B103 0800 | CHP | IF,WJV3,0800 | 366 |
| 06EA | C101 06AE | BRCL | GT,WJ603 | 367 |
| 06BC | 2013 | CHP | IS,WJV3,1 | 368 |
| 06BD | C104 06AE | BRCL | LT,WJ603 | 369 |
| 06BF | 9C03 09EE | STR | D,WJV3,WJSFIN | 370 |
| 06C1 | | DS | 16 SPACE FOR PATCHES | 371 |
| | | | | 372 |

ATAC

LOC

OBJECT CODE

CARD IMAGE

. OUTPUT TITLE AND GET SIGNAL STRENGTH

WJ604 LDR I, WJ604, WJSSS GET ADDRESS OF SIGNAL STRENGTH TITLE

WJ604 BAL I, RET, WJ90 OUTPUT TILE AND GET ENTRY

WJ604 BRCL U, WJ604, 0100 ILLEGAL ENTRY

WJ604 CMP I, WJ604, 0100 COMPARE WITH MAX VALUE

WJ604 BRCL I, WJ604, 0100 ILLEGAL - TOO LARGE

WJ604 BRCL I, WJ604, 0100 COMPARE COUNT WITH MAX

WJ604 BRCL I, WJ604, 0100 ILLEGAL - TOO LARGE

WJ604 BRCL I, WJ604, 0100 PUT SIGNAL STRENGTH IN REG 2

WJ604 BRCL I, WJ604, 0100 CONVERT BCD TO HEX

WJ604 BRCL I, WJ604, 0100 MULTIPLY BY 127

WJ604 BRCL I, WJ604, 0100 ROUND

WJ604 BRCL I, WJ604, 0100 SET UP DIVISOR

WJ604 BRCL I, WJ604, 0100 DIVIDE BY 100

WJ604 BRCL I, WJ604, 0100 CLEAR ALL BUT VALUE WANTED

WJ604 BRCL I, WJ604, 0100 STORE DESIRED SIGNAL STRENGTH

WJ604 BRCL I, WJ604, 0100 SPACE FOR PATCHES

: OUTPUT FREQUENCY TO RECEIVER AND GET RESPONSE

WJ605 LDR D, WJ605, WJSFLL GET LOWER LIMIT (LOWER)

WJ605 LDR D, WJ605, WJSFLL GET LOWER LIMIT (UPPER)

WJ605 LDR D, WJ605, WJSFLL POSITION FREQUENCY FOR WJ

WJ605 LDR D, WJ605, WJSFLL GET CONTROL WORD 2 (FREQ LOWER)

WJ605 LDR D, WJ605, WJSFLL GET CONTROL WORD 1 (FREQ UPPER)

WJ605 LDR D, WJ605, WJSFLL CLEAR OLD FREQ (LOWER)

WJ605 LDR D, WJ605, WJSFLL CLEAR OLD FREQ (UPPER)

WJ605 LDR D, WJ605, WJSFLL PUT IN NEW FREQ (LOWER)

WJ605 LDR D, WJ605, WJSFLL PUT IN NEW FREQ (UPPER)

WJ605 LDR D, WJ605, WJSFLL SAVE NEW CONTROL WORD 2

WJ605 LDR D, WJ605, WJSFLL SAVE NEW CONTROL WORD 1

WJ605 LDR D, WJ605, WJSFLL SEND /RECEIVE NEW CONTROL WORD

WJ605 LDR D, WJ605, WJSFLL SPACE FOR PATCHES

: COMPARE RECEIVED SIGNAL STRENGTH WITH SET SIGNAL STRENGTH

WJ605 LDR D, WJ605, WJSFLL GET RECEIVED WORD 4

WJ605 LDR D, WJ605, WJSFLL CLEAR ALL BUT SIGNAL STRENGTH

WJ605 LDR D, WJ605, WJSFLL GET SPECIFIED SIGNAL STRENGTH

WJ605 LDR D, WJ605, WJSFLL COMPARE RECEIVED WITH SPECIFIED

WJ605 LDR D, WJ605, WJSFLL FIND

WJ605 LDR D, WJ605, WJSFLL SPACE FOR PATCHES

: INCREMENT FREQUENCY AND TRY AGAIN

WJ605 LDR D, WJ605, WJSFLL GET LAST FREQ (LOWER)

WJ605 LDR D, WJ605, WJSFLL GET LAST FREQ (UPPER)

WJ605 LDR D, WJ605, WJSFLL POSITION FOR INCREMENT

WJ605 LDR D, WJ605, WJSFLL CLEAR ALL BUT FREQUENCY

WJ605 LDR D, WJ605, WJSFLL GET INCREMENT

WJ605 LDR D, WJ605, WJSFLL CLEAR REGISTER

WJ605 LDR D, WJ605, WJSFLL INCREMENT FREQUENCY

WJ605 LDR D, WJ605, WJSFLL INCREMENT FREQUENCY

WJ605 LDR D, WJ605, WJSFLL INCREMENT FREQUENCY

WJ605 LDR D, WJ605, WJSFLL INCREMENT FREQUENCY

WJ605 LDR D, WJ605, WJSFLL INCREMENT FREQUENCY

WJ605 LDR D, WJ605, WJSFLL INCREMENT FREQUENCY

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| LOC | OBJECT CODE | CARD IMAGE | CARDNUM |
|------|-------------|--|---------|
| 072C | E201 09EB | LDR D,WJVI,WJSF00 | 426 |
| 072E | E202 09EA | LDR D,WJVI,WJSF00 | 427 |
| 0730 | E062 | CMP R,WJVS,WJVS | 428 |
| 0731 | C101 06F2 | BRCL R,WJVS,WJVS | 429 |
| 0733 | C102 073F | BRCL R,WJVS,WJVS | 430 |
| 0735 | E201 09F0 | LDR D,WJVI,WJSCNT | 431 |
| 0737 | E011 | ADD IS,WJVI,100 | 432 |
| 0738 | E011 | ADD IS,WJVI,100 | 433 |
| 0739 | C101 075E | BRCL R,WJVS,WJVS | 434 |
| 073B | C101 09F0 | STR D,WJVI,WJSCNT | 435 |
| 073D | C107 06EE | BRCL R,WJVS,WJVS | 436 |
| 073F | E051 | CMP R,WJVI,WJVS | 437 |
| 0740 | C104 0735 | BRCL R,WJVS,WJVS | 438 |
| 0742 | C107 06F2 | BRCL R,WJVS,WJVS | 439 |
| 0744 | | DS 10 | 440 |
| | | . FIND | 441 |
| | | . WJ609 | 442 |
| C74E | BC11 09E0 | LDRM D,WJVI,WJRF,2 | 443 |
| 0750 | 9C11 09D8 | STRM D,WJVI,WJCF,2 | 444 |
| 0752 | C107 05DE | BRCL U,WJ40 | 445 |
| 0754 | | DS 10 | 446 |
| | | . NO FIND - TELL OPERATOR | 447 |
| | | . WJ610 | 448 |
| 075E | BC11 09E0 | LDRM D,WJVI,WJRF,2 | 449 |
| 0760 | 9C11 09D8 | STRM D,WJVI,WJCF,2 | 450 |
| 0762 | E101 0ECA | LDR I,WJVI,WJSNF | 451 |
| 0764 | E000 0230 | FAL I,RET,OUTPUT | 452 |
| 0766 | C107 0519 | BRCL U,WJ02 | 453 |
| 0768 | | DS 40 | 454 |
| | | . ***** | 455 |
| | | . RECEIVE CONTROL WORD FROM RECEIVER (COMMAND=6) | 456 |
| | | . ***** | 457 |
| | | . ***** | 458 |
| | | . ***** | 459 |
| | | . ***** | 460 |
| | | . ***** | 461 |
| | | . ***** | 462 |
| | | . ***** | 463 |
| | | . ***** | 464 |
| 0790 | E101 0001 | LDR I,WJVI,01 | 465 |
| 0792 | 9C01 09F4 | STR D,WJVI,CHKFLG1 | 466 |
| 0794 | E000 08E1 | BAL I,RET,WJR | 467 |
| 0796 | E101 0000 | LDR I,WJVI,0 | 468 |
| 0798 | 9C01 09F4 | STR D,WJVI,CHKFLG1 | 469 |
| 079A | C107 05DE | BRCL U,WJ40 | 470 |
| 079C | C107 0519 | BRCL U,WJ02 | 471 |
| 079E | | DS 10 | 472 |
| | | . ***** | 473 |
| | | . ***** | 474 |
| | | . ***** | 475 |
| | | . ***** | 476 |
| | | . ***** | 477 |
| | | . ***** | 478 |

ATAC
LOC
OBJECT CODE
CARD IMAGE

CARDNUM

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. *****
. WJ80      LLC      D,WJV1,WJCRF      LOAD "ADDRESS OFF"
.          ROUT      02,WJV1          TURN OFF INTERFACE
.          LDR       R,RET,WJRET      LOAD RETURN ADDRESS
.          BRC       R,RET           RETURN TO CALLER
.
.
. *****
. ROUTINE TO OUTPUT BUFFER AND GET ENTRY FROM KEYBOARD
. ENTRY: WJV1 ADDRESS OF BUFFER TO BE OUTPUT
. EXIT:  WJV2 NUMBER OF CHARACTERS ENTERED
.        WJV3 ENTRY {LOWER} BCD
.        WJV4 ENTRY {UPPER} BCD
. RETURN FOR ILLEGAL ENTRY
. RETURN+2 FOR ILLEGAL ENTRY
. *****
. WJ90      STR      D,RET,WJ90R      SAVE THE RETURN ADDRESS
.          BAL      I,RET,OUTPUT     OUTPUT BUFFER
.          LDR      IS,WJ63,0        GET INPUT FROM KEYBOARD
.          LDR      IS,WJV4,0        CLEAR REGISTER
.          LDR      IS,WJV5,0        CLEAR REGISTER
.          RX       WJV2,WJV5,WJV1   GET COUNT FROM KEYMR
.          LDR      R,WJV7,WJV2     SET UP INDEX FOR LOOP
.          LDR      D,RET,WJ90R     GET RETURN ADDRESS
.          ADD      IS,WJV7,-1       DECREMENT COUNT
.          BRC      R,4,RET          ILLEGAL IF NEG NOTHING ENTERED
.          ADD      IS,WJV1,1        INCREMENT BUFFER POINTER
.          LDR      RX,WJV6,WJV5,WJV1 GET ONE CHARACTER
.          IS       WJV6,030        COMPARE CHAR WITH ASCII ZERO
.          BRC      R,4,RET          ILLEGAL CHARACTER IS LESS
.          IS       WJV6,039        COMPARE WITH ASCII 9
.          BRC      R,1,RET          ILLEGAL CHARACTER IS GREATER
.          AND      I,WJV6,OF       CLEAR UPPER 12 BITS
.          SHD      LL,WJV3,4       POSITION ENTRY FOR NEW CHARACTER
.          LDR      R,WJV3,WJV6     OR IN NEW CHARACTER
.          ADD      IS,WJV7,-1       DECREMENT COUNT
.          NN       WJ90,1          KEEP GOING IF MORE CHARACTERS
.          BRC      IS,RET,2        INCREMENT RETURN ADDRESS
.          BRC      R,RET          LEGAL RETURN
.
.
. *****
. ROUTINE TO CHECK A FREQUENCY ENTRY AND MAKE SURE IT IS IN WJ
. FREQUENCY LIMITS
. ENTRY: WJV2 NUMBER OF CHARACTERS ENTERED
.        WJV3 FREQUENCY (LOWER) BCD,1 HZ RESOLUTION

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|------|-------------|---------------------|---------|
| 0833 | 4002 | LDR IS,WJV2,0 | 638 |
| 0834 | F831 | SHD LL,WJV1,4 | 639 |
| 0835 | AE32 | SHS LL,WJV2,4 | 640 |
| 0836 | F831 | SHD LL,WJV1,4 | 641 |
| 0837 | A502 | IOR I,WJV2,63030 | 642 |
| 0839 | 9C02 | STR D,WJV2,WJDBF1 | 643 |
| 083B | 4002 | LDR JS,WJV2,0 | 644 |
| 083C | F831 | SHD LL,WJV1,4 | 645 |
| 083D | AE32 | SHS LL,WJV2,4 | 646 |
| 083E | F831 | SHD LL,WJV1,4 | 647 |
| 083F | A502 | IOR I,WJV2,63030 | 648 |
| 0841 | 9C02 | STR D,WJV2,WJDBF1+1 | 649 |
| 0843 | 0AF6 | LDR I,WJV1,WJDBF | 650 |
| 0845 | E101 | BAI I,RET,OUTPUT | 651 |
| 0847 | E200 | LDR D,RET,WJ90R | 652 |
| 0849 | BF07 | BRC R,7,RET | 653 |
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| 084A | 9C00 | STR D,RET,WJ90R | 663 |
| 084C | 4002 | LDR IS,WJV2,0 | 664 |
| 084D | F871 | SHD LL,WJV1,8 | 665 |
| 084E | A502 | IOR I,WJV2,62030 | 666 |
| 0850 | 2030 | STR D,WJV2,WJDRF1 | 667 |
| 0852 | 4002 | LDR IS,WJV2,0 | 668 |
| 0853 | F831 | SHD LL,WJV1,4 | 669 |
| 0854 | AE32 | SHS LL,WJV2,4 | 670 |
| 0855 | F831 | SHD LL,WJV1,4 | 671 |
| 0856 | A502 | IOR I,WJV2,63030 | 672 |
| 0858 | 9C02 | STR D,WJV2,WJDRF1+1 | 673 |
| 085A | 0B14 | LDR I,WJV1,WJDRF | 674 |
| 085C | E101 | BAI I,RET,OUTPUT | 675 |
| 085E | E200 | LDR D,RET,WJ90R | 676 |
| 0860 | BF07 | BRC R,7,RET | 677 |
| | | | 678 |
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| IOC | OBJECT CODE | CARD IMAGE | SAVE RETURN ADDRESS SET UP MASK CLEAR REGISTER DO 1ST DIGIT DO 2ND DIGIT DO 3RD DIGIT DO 4TH DIGIT DO 5TH DIGIT DO 6TH DIGIT DO 7TH DIGIT | PAGE |
|------|-------------|------------|--|------|
| C861 | 9C00 | 09F1 | STR | 691 |
| C863 | E109 | 000F | LDR | 692 |
| C865 | 4005 | | LDR | 693 |
| C866 | 4006 | | LDR | 694 |
| C867 | E017 | | LDR | 695 |
| C868 | E038 | 08A4 | EAL | 696 |
| C869 | E000 | | LDR | 697 |
| C86A | E075 | | LDR | 698 |
| C86B | E017 | | LDR | 699 |
| C86C | AD37 | | SHS | 700 |
| C86D | AD37 | | SHS | 701 |
| C86E | E087 | | ADD | 702 |
| C86F | E038 | | LDR | 703 |
| C870 | AD38 | | SHS | 704 |
| C871 | ED00 | 08A4 | EAL | 705 |
| C873 | AE37 | | SHS | 706 |
| C874 | AE37 | | SHS | 707 |
| C875 | E017 | | LDR | 708 |
| C876 | AD77 | | SHS | 709 |
| C877 | E087 | | ADD | 710 |
| C878 | E038 | | LDR | 711 |
| C879 | AD78 | | SHS | 712 |
| C87A | ED00 | 08A4 | EAL | 713 |
| C87C | AE77 | | SHS | 714 |
| C87D | AE75 | | LDR | 715 |
| C87E | E017 | | LDR | 716 |
| C87F | ADE7 | | SHS | 717 |
| C880 | E087 | | ADD | 718 |
| C881 | E038 | | LDR | 719 |
| C882 | AD88 | | SHS | 720 |
| C883 | ED00 | 08A4 | EAL | 721 |
| C885 | AE87 | | SHS | 722 |
| C886 | AE75 | | LDR | 723 |
| C887 | E027 | | LDR | 724 |
| C888 | E087 | | ADD | 725 |
| C889 | E048 | | LDR | 726 |
| C88A | ED00 | 08A4 | EAL | 727 |
| C88C | E076 | | LDR | 728 |
| C88D | E027 | | LDR | 729 |
| C88E | AD37 | | SHS | 730 |
| C88F | E087 | | ADD | 731 |
| C890 | E048 | | LDR | 732 |
| C891 | AD38 | | SHS | 733 |
| C892 | ED00 | 08A4 | EAL | 734 |
| C894 | AE37 | | SHS | 735 |
| C895 | AE76 | | LDR | 736 |
| C896 | E027 | | LDR | 737 |
| C897 | AD77 | | SHS | 738 |
| C898 | E087 | | ADD | 739 |
| C899 | E048 | | LDR | 740 |
| C89A | AD78 | | SHS | 741 |
| C89B | ED00 | 08A4 | EAL | 742 |
| C89D | AE77 | | SHS | 743 |

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| LOC | OBJECT CODE | CARD | IMAGE | CARDNUM |
|------|-------------|------|----------------|---------|
| 0977 | 9C01 09E9 | STR | D, WJVI, WJR3 | 903 |
| 0979 | E200 09F1 | LDR | D, RET, WJ90R | 904 |
| 097B | BF07 | ERC | R, 7, RET | 905 |
| 097C | CD01 0064 | MUL | I, WJVI, 064 | 906 |
| 097E | 6371 | ADD | IS, WJVI, 037 | 907 |
| 097F | 47F3 | LDR | IS, WJVI, 07F3 | 908 |
| 0980 | PC31 | DIV | R, WJVI, WJ3 | 909 |
| 0981 | E021 | LDR | R, WJVI, WJ2 | 910 |
| 0982 | 4002 | LDR | IS, WJ2, 0 | 911 |
| 0983 | 40A3 | LDR | IS, WJ3, 10 | 912 |
| 0984 | FC31 | DIV | R, WJVI, WJ3 | 913 |
| 0985 | E014 | LDR | R, WJVI, WJ1 | 914 |
| 0986 | E021 | LDR | R, WJVI, WJ2 | 915 |
| 0987 | 4002 | LDR | IS, WJ2, 0 | 916 |
| 0988 | FC31 | DIV | R, WJVI, WJ3 | 917 |
| 0989 | AE31 | SHS | LL, WJVI, 4 | 918 |
| 098A | AE72 | SHS | LL, WJ2, 8 | 919 |
| 098B | A441 | IOR | R, WJVI, WJ4 | 920 |
| 098C | A421 | IOR | R, WJVI, WJ2 | 921 |
| 098D | BF07 | BRC | R, 7, RET | 922 |
| | | | | 923 |
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| LOC | OBJECT CODE | CARD IMAGE | RCVR CONTROL WORD | CARDNUM |
|------|-------------|--------------------------------------|---------------------------------|---------|
| 09C8 | | WJCH1 DS 1 | 1 | 1008 |
| 09C9 | | WJCH2 DS 1 | 2 | 1009 |
| 09CA | | WJCH3 DS 1 | 3 | 1010 |
| 09CB | | WJCH4 DS 1 | 4 | 1011 |
| | | RECEIVED WORD FROM WJ | | |
| 09CC | | WJRS1 DS 1 | 1 | 1012 |
| 09CD | | WJRS2 DS 1 | 2 | 1013 |
| 09CE | | WJRS3 DS 1 | 3 | 1014 |
| 09CF | | WJRS4 DS 1 | 4 | 1015 |
| | | TENTATIVE VALUES - DISPLAY FORMAT | | |
| 09D0 | | WJFL DS 1 | TENTATIVE FREQ (LOWER) | 1016 |
| 09D1 | | WJFU DS 1 | TENTATIVE FREQ (UPPER) | 1017 |
| 09D2 | | WJFD DS 1 | TENTATIVE DETECT MODE | 1018 |
| 09D3 | | WJBL DS 1 | TENTATIVE IFO FREQ (LOWER) | 1019 |
| 09D4 | | WJBU DS 1 | TENTATIVE IFO FREQ (UPPER) | 1020 |
| 09D5 | | WJGM DS 1 | TENTATIVE GAIN MODE | 1021 |
| 09D6 | | WJRG DS 1 | TENTATIVE RF GAIN | 1022 |
| 09D7 | | WJIF DS 1 | TENTATIVE IF BANDWIDTH | 1023 |
| | | CONTROL VALUES - DISPLAY FORMAT | | |
| 09D8 | | WJCP1 DS 1 | CONTROL FREQ (LOWER) | 1024 |
| 09D9 | | WJCU DS 1 | CONTROL FREQ (UPPER) | 1025 |
| 09DA | | WJCD DS 1 | CONTROL DETECT MODE | 1026 |
| 09DB | | WJCB DS 1 | CONTROL BFO FREQ (LOWER) | 1027 |
| 09DC | | WJCB DS 1 | CONTROL BFO FREQ (UPPER) | 1028 |
| 09DD | | WJCG DS 1 | CONTROL GAIN MODE | 1029 |
| 09DE | | WJCG DS 1 | CONTROL RF GAIN | 1030 |
| 09DF | | WJCF DS 1 | CONTROL IF BANDWIDTH | 1031 |
| | | RECEIVED VALUES - DISPLAY FORMAT | | |
| 09E0 | | WJFL DS 1 | RECEIVED FREQ (LOWER) | 1032 |
| 09E1 | | WJFU DS 1 | RECEIVED FREQ (UPPER) | 1033 |
| 09E2 | | WJFD DS 1 | RECEIVED DETECT MODE | 1034 |
| 09E3 | | WJBL DS 1 | RECEIVED BFO FREQ (LOWER) | 1035 |
| 09E4 | | WJBU DS 1 | RECEIVED BFO FREQ (UPPER) | 1036 |
| 09E5 | | WJGM DS 1 | RECEIVED GAIN MODE | 1037 |
| 09E6 | | WJRG DS 1 | RECEIVED RF GAIN | 1038 |
| 09E7 | | WJIF DS 2 | RECEIVED IF BANDWIDTH | 1039 |
| 09E9 | | WJRS DS 1 | RECEIVED SIGNAL STRENGTH | 1040 |
| | | SCAN VARIABLES | | |
| 09EA | | WJSFU DS 1 | UPPER LIMIT (UPPER) | 1041 |
| 09EB | | WJSFU DS 1 | UPPER LIMIT (LOWER) | 1042 |
| 09EC | | WJSFU DS 1 | LOWER LIMIT (UPPER) | 1043 |
| 09ED | | WJSFU DS 1 | LOWER LIMIT (LOWER) | 1044 |
| 09EE | | WJSFI DS 1 | FREQUENCY INCREMENT (8 KHZ MAX) | 1045 |
| 09EF | | WJSFI DS 1 | SIGNAL STRENGTH | 1046 |
| 09F0 | | WJSCT DS 1 | COUNT OF NUMBER OF SCAN PASSES | 1047 |
| | | SAVE LOCATIONS FOR ALL WJ9- ROUTINES | | |
| 09F1 | | WJ91 DS 1 | | 1048 |
| 09F2 | | WJ92 DS 1 | | 1049 |
| 09F3 | | WJ93 DS 1 | | 1050 |
| 09F4 | | WJ94 DS 1 | | 1051 |
| 09F5 | | WJ95 DS 1 | | 1052 |
| 09F6 | | WJ96 DS 1 | | 1053 |
| 09F7 | | WJ97 DS 1 | | 1054 |
| 09F8 | | WJ98 DS 1 | | 1055 |
| 09F9 | | WJ99 DS 1 | | 1056 |
| 09FA | | WJ9A DS 1 | | 1057 |
| 09FB | | WJ9B DS 1 | | 1058 |
| 09FC | | WJ9C DS 1 | | 1059 |
| 09FD | | WJ9D DS 1 | | 1060 |

CARDNUM

CARD IMAGE

OBJECT CODE

LOC

| | | | | | |
|------|------|----|-------|-------|------|
| 0A21 | 5245 | DC | 05245 | RE | 1114 |
| 0A22 | 4345 | DC | 04345 | CE | 1115 |
| 0A23 | 4956 | DC | 04956 | IV | 1116 |
| 0A24 | 4544 | DC | 04544 | ED | 1117 |
| 0A25 | 0D0A | DC | 0D0A | CR/LF | 1118 |
| 0A26 | 343D | EC | 0343D | 4= | 1119 |
| 0A27 | 454E | EC | 0454E | EN | 1120 |
| 0A28 | 5445 | DC | 05445 | TE | 1121 |
| 0A29 | 5220 | DC | 05220 | R | 1122 |
| 0A2A | 5445 | DC | 05445 | TE | 1123 |
| 0A2B | 4E54 | EC | 04E54 | NT | 1124 |
| 0A2C | 4154 | DC | 04154 | AT | 1125 |
| 0A2D | 4956 | EC | 04956 | IV | 1126 |
| 0A2E | 450D | EC | 0450D | E/CR | 1127 |
| 0A2F | 0A35 | EC | 00A35 | LF/5 | 1128 |
| 0A30 | 3D53 | EC | 03D53 | =5 | 1129 |
| 0A31 | 4341 | EC | 04341 | CA | 1130 |
| 0A32 | 4E20 | EC | 04E20 | N | 1131 |
| 0A33 | 0D0A | DC | 0D0A | CR/LF | 1132 |
| 0A34 | 363D | DC | 0363D | 6= | 1133 |
| 0A35 | 5245 | EC | 05245 | RE | 1134 |
| 0A36 | 4345 | DC | 04345 | CE | 1135 |
| 0A37 | 4956 | EC | 04956 | IV | 1136 |
| 0A38 | 4520 | DC | 04520 | E | 1137 |
| 0A39 | 434F | DC | 0434F | CO | 1138 |
| 0A3A | 4E54 | DC | 04E54 | NT | 1139 |
| 0A3B | 524F | DC | 0524F | RO | 1140 |
| 0A3C | 4C20 | DC | 04C20 | L | 1141 |
| 0A3D | 0D0A | EC | 0D0A | CR/LF | 1142 |
| 0A3E | 373D | DC | 0373D | 7= | 1143 |
| 0A3F | 444F | DC | 0444F | DO | 1144 |
| 0A40 | 4E45 | DC | 04E45 | NE | 1145 |
| 0A41 | 0D0A | EC | 0D0A | CR/LF | 1146 |
| 0A42 | 383D | EC | 0383D | 8= | 1147 |
| 0A43 | 5245 | DC | 05245 | RE | 1148 |
| 0A44 | 494E | DC | 0494E | IN | 1149 |
| 0A45 | 4954 | DC | 04954 | IT | 1150 |
| 0A46 | 494C | DC | 0494C | IL | 1151 |
| 0A47 | 495A | DC | 0495A | IZ | 1152 |
| 0A48 | 4520 | DC | 04520 | E | 1153 |
| 0A49 | 0D0A | EC | 0D0A | CR/LF | 1154 |
| 0A4A | 0000 | DC | 0 | NULL | 1155 |
| 0A4B | 0006 | DC | 6 | COUNT | 1156 |
| 0A4C | 4652 | DC | 04652 | FR | 1157 |
| 0A4D | 4551 | DC | 04551 | EQ | 1158 |
| 0A4E | 2028 | DC | 02028 | H2 | 1159 |
| 0A4F | 485A | DC | 0485A | H2 | 1160 |
| 0A50 | 2920 | DC | 02920 |) | 1161 |
| 0A51 | 0D0A | DC | 0D0A | CR/LF | 1162 |
| 0A52 | 0000 | DC | 0 | NULL | 1163 |
| | | | | | 1164 |
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WJFB
FREQUENCY
TITLE BUFFER

| ATAC | LOC | OBJECT CODE | CARD IMAGE | TIME: 16:43:42 | 03/18/77 | PAGE |
|------|-----|-------------|------------|----------------|----------|---------|
| | | | | | | CARDNUM |
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| | | | | | | IN |
| | | | | | | H |
| | | | | | | OD |
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| | | | | | | CCR |
| | | | | | | LF3 |
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| | | | | | | CR/LF |
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| | | | | | | K |
| | | | | | | HZ |
| | | | | | | CR/LF |
| | | | | | | 4= |
| | | | | | | 8 |
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| | | | | | | AN |
| | | | | | | DW |
| | | | | | | ID |
| | | | | | | TH |
| | | | | | | CR/LF |
| | | | | | | I= |
| | | | | | | 50 |
| | | | | | | 0 |
| | | | | | | HZ |
| | | | | | | CR/LP |
| | | | | | | 2= |
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| | | | | | | KH |
| | | | | | | ZCR |
| | | | | | | LF3 |
| | | | | | | =4 |
| | | | | | | K |
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| | | | | | | CR/LF |
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03/18/77

TIME: 16:43:42

ATAC

CARDNUM

CARD IMAGE

OBJECT CODE

LOC

KH
Z
CR/LF
NULL

DC 04B48
DC 05A20
LC 00D0A
DC 0

0A85 4B48
0A86 5A20
0A87 0D0A
0A88 0000

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: DETECT MODE TITLE BUFFER

COUNT

WJDBE DC 40
DC 04445
DC 05445
DC 04354
DC 0204D
DC 04F44
DC 0450D
DC 00A30
DC 03D41
DC 04D0D
DC 00A31
DC 03D46
DC 04D0D
DC 00A32
DC 03D42
DC 0464F
DC 02046
DC 04958
DC 04544
DC 00D0A
DC 0333D
DC 04246
DC 04F20
DC 05641
DC 0520D
DC 00A34
DC 03D49
DC 05342
DC 00D0A
DC 0353D
DC 04C53
DC 0420D
DC 00A36
DC 03D55
DC 05342
DC 00D0A
DC 0373D
DC 0414D
DC 02D4E
DC 04C20
DC 00D0A
DC 0

TE
CT
H
OD
ECR
LFO
=A
MCR
LF1
=F
MCR
LF2
=B
PO
F
LY
ED
CR/LP
3=
BF
O
VA
RCR
LF4
=1
SB
CR/LP
5=
LS
BCR
LF6
=U
SB
CR/LF
7=
AM
-N
L
CR/LF
NULL

0A89 0028
0A8A 4445
0A8B 5445
0A8C 4354
0A8D 204D
0A8E 4F44
0A8F 450D
0A90 0A30
0A91 3D41
0A92 4D0D
0A93 0A31
0A94 3D46
0A95 4D0D
0A96 0A32
0A97 3D42
0A98 464F
0A99 2046
0A9A 4958
0A9B 4544
0A9C 0D0A
0A9D 333D
0A9E 4246
0A9F 4F20
0AA0 5641
0AA1 520D
0AA2 0A34
0AA3 3D49
0AA4 5342
0AA5 0D0A
0AA6 353D
0AA7 4C53
0AA8 420D
0AA9 0A36
0AAA 3D55
0AAB 5342
0AAC 0D0A
0AAD 373D
0AAE 414D
0AAF 2D4E
0AE0 4C20
0AE1 0D0A
0AE2 0000

1270
1271
1272

: BFO FREQUENCY TITLE BUFFER

COUNT

WJDBE DC 21
DC 04246
DC 04F20

0AB3 0015
0AB4 4246
0AB5 4F20

| LOC | OBJECT CODE | CARD IMAGE | COUNT | CARDNUM |
|------|-------------|------------|-------|---------|
| 0AE5 | 0007 | DC | DE | 1326 |
| 0AE6 | 4445 | DC | TE | 1327 |
| 0AE7 | 5445 | DC | CT | 1328 |
| 0AE8 | 4354 | DC | M | 1329 |
| 0AE9 | 204D | DC | OD | 1330 |
| 0AEA | 4F44 | DC | E | 1331 |
| 0AEB | 4520 | DC | = | 1332 |
| 0AEC | 3D20 | DC | NULL | 1333 |
| 0AED | 0000 | DC | | 1334 |
| | | | | 1335 |
| | | | | 1336 |
| 0AEE | 000B | DC | BF | 1337 |
| 0AEF | 4246 | DC | O | 1338 |
| 0AF0 | 4F20 | DC | FR | 1339 |
| 0AF1 | 4652 | DC | EQ | 1340 |
| 0AF2 | 4551 | DC | = | 1341 |
| 0AF3 | 203D | DC | 4 | 1342 |
| 0AF4 | 2034 | DC | XX | 1343 |
| 0AF5 | 2020 | DC | XX | 1344 |
| 0AF6 | 2020 | DC | O | 1345 |
| 0AF7 | 3020 | DC | RZ | 1346 |
| 0AF8 | 485A | DC | CR/LF | 1347 |
| 0AF9 | 000A | DC | NULL | 1348 |
| 0AFA | 0000 | DC | | 1349 |
| | | | | 1350 |
| 0AFB | 0006 | DC | GA | 1351 |
| 0AFC | 4741 | DC | IN | 1352 |
| 0AFD | 494E | DC | M | 1353 |
| 0AEE | 204D | DC | OD | 1354 |
| 0AEF | 4F44 | DC | E | 1355 |
| 0B00 | 4520 | DC | = | 1356 |
| 0B01 | 3D20 | DC | NULL | 1357 |
| 0B02 | 0000 | DC | | 1358 |
| | | | | 1359 |
| 0B03 | 0008 | DC | IF | 1360 |
| 0B04 | 4946 | DC | B | 1361 |
| 0B05 | 2042 | DC | AN | 1362 |
| 0B06 | 414E | DC | AW | 1363 |
| 0B07 | 4457 | DC | DW | 1364 |
| 0B08 | 4944 | DC | ID | 1365 |
| 0B09 | 5448 | DC | TH | 1366 |
| 0B0A | 203D | DC | = | 1367 |
| 0B0B | 2020 | DC | NULL | 1368 |
| 0B0C | 0000 | DC | | 1369 |
| | | | | 1370 |
| 0B0D | 0009 | DC | RF | 1371 |
| 0B0E | 5246 | DC | G | 1372 |
| 0B0F | 2047 | DC | AI | 1373 |
| 0B10 | 4149 | DC | N | 1374 |
| 0B11 | 4E20 | DC | | 1375 |
| | | | | 1376 |
| | | | | 1377 |

| ATAC | IOC | OBJECT CODE | CARD IMAGE | CARDNUM |
|-------------------------------------|-------|-------------|------------|---------|
| OB12 | 3D20 | | DC 03D20 | 1379 |
| OB13 | 2020 | WJDRF1 | DC XX | 1380 |
| OB14 | 2020 | | DC XX | 1381 |
| OB15 | 2520 | | DC XX | 1382 |
| OB16 | 0D0A | | DC CR/LF | 1383 |
| OB17 | 0000 | | DC NULL | 1384 |
| OB18 | 000D | | | 1385 |
| OB19 | 5349 | | | 1386 |
| OB1A | 474E | | | 1387 |
| OB1B | 414C | | | 1388 |
| OB1C | 2053 | | | 1389 |
| OB1D | 5452 | | | 1390 |
| OB1E | 454E | | | 1391 |
| OB1F | 4754 | | | 1392 |
| OB20 | 4820 | | | 1393 |
| OB21 | 3D20 | | | 1394 |
| OB22 | 2020 | | | 1395 |
| OB23 | 2020 | | | 1396 |
| OB24 | 2520 | | | 1397 |
| OB25 | 0D0A | | | 1398 |
| OB26 | 0000 | | | 1399 |
| OB27 | 0B2B | | | 1400 |
| OB28 | 0B32 | | | 1401 |
| OB29 | 0B34 | | | 1402 |
| OB2A | 0B3C | | | 1403 |
| OB2B | 0005 | | | 1404 |
| OB2C | 484F | | | 1405 |
| OB2D | 4C44 | | | 1406 |
| OB2E | 2041 | | | 1407 |
| OB2F | 4743 | | | 1408 |
| OB30 | 0D0A | | | 1409 |
| OB31 | 0000 | | | 1410 |
| OB32 | FFFF | | | 1411 |
| OB33 | 0000 | | | 1412 |
| OB34 | 0006 | | | 1413 |
| OB35 | 4E4F | | | 1414 |
| OB36 | 524D | | | 1415 |
| OB37 | 414C | | | 1416 |
| OB38 | 2041 | | | 1417 |
| OB39 | 4743 | | | 1418 |
| OB3A | 0D0A | | | 1419 |
| OB3B | 0000 | | | 1420 |
| . SIGNAL STRENGTH TITLE FOR DISPLAY | | | | 1421 |
| WJDSS | 13 | | | 1422 |
| | 05349 | | | 1423 |
| | 0474E | | | 1424 |
| | 0414C | | | 1425 |
| | 02053 | | | 1426 |
| | 05452 | | | 1427 |
| | 0454E | | | 1428 |
| | 04754 | | | 1429 |
| | 04820 | | | 1430 |
| | 03D20 | | | 1431 |
| | 02020 | | | |
| | 02020 | | | |
| | 02520 | | | |
| | 0D0A | | | |
| | 0 | | | |
| . TABLE OF GAIN MODES FOR DISPLAY | | | | |
| WJGMIB | LC | | | |
| | WJGM0 | | | |
| | WJGM1 | | | |
| | WJGM2 | | | |
| | WJGM3 | | | |
| WJGM0 | 5 | | | |
| | 0484F | | | |
| | 04C44 | | | |
| | 02041 | | | |
| | 04743 | | | |
| | 0D0A | | | |
| | 0 | | | |
| WJGM1 | -1 | | | |
| | 0 | | | |
| WJGM2 | 6 | | | |
| | 04E4F | | | |
| | 0524D | | | |
| | 0414C | | | |
| | 02041 | | | |
| | 04743 | | | |
| | 0D0A | | | |
| | 0 | | | |

| LOC | OBJECT CODE | CARD IMAGE | CARD RUN | PAGE |
|------|-------------|------------|---------------------|------|
| 0B3C | 0004 | WJGM3 | DC | 1432 |
| 0B3D | 4D41 | 4 | COUNT/HANUAL | 1433 |
| 0B3E | 4E55 | 04D41 | MA | 1434 |
| 0B3F | 414C | 04E55 | NU | 1435 |
| 0B40 | 0D0A | 414C | AL | 1436 |
| 0B41 | 0000 | 0D0A | CR/LF | 1437 |
| | | 0 | NULL | 1438 |
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| 0B42 | 0B47 | WJIF0 | DC | 1442 |
| 0B43 | 0B49 | WJIF1 | NOT USED | 1443 |
| 0B44 | 0B4F | WJIF2 | 500HZ | 1444 |
| 0B45 | 0B55 | WJIF3 | 2KHZ | 1445 |
| 0B46 | 0B5B | WJIF4 | 4KHZ | 1446 |
| | | | 8KHZ | 1447 |
| | | | | 1448 |
| 0B47 | FFFF | WJIF0 | DC | 1449 |
| 0B48 | 0000 | -1 | COUNT/INVALID ENTRY | 1450 |
| | | 0 | NULL | 1451 |
| | | | | 1452 |
| 0B49 | 0004 | WJIF1 | DC | 1453 |
| 0B4A | 3530 | 4 | COUNT/500KHZ | 1454 |
| 0B4B | 3020 | 03530 | 50 | 1455 |
| 0B4C | 485A | 03020 | 0 | 1456 |
| 0B4D | 0D0A | 485A | HZ | 1457 |
| 0B4E | 0000 | 0D0A | CR/LF | 1458 |
| | | 0 | NULL | 1459 |
| | | | | 1460 |
| 0B4F | 0004 | WJIF2 | DC | 1461 |
| 0B50 | 3220 | 4 | COUNT/2KHZ | 1462 |
| 0B51 | 4B48 | 03220 | 2 | 1463 |
| 0B52 | 5A20 | 04B48 | KH | 1464 |
| 0B53 | 0D0A | 05A20 | Z | 1465 |
| 0B54 | 0000 | 0D0A | CR/LD | 1466 |
| | | 0 | NULL | 1467 |
| | | | | 1468 |
| 0B55 | 0004 | WJIF3 | DC | 1469 |
| 0B56 | 3420 | 4 | COUNT/4KHZ | 1470 |
| 0B57 | 4B48 | 03420 | 4 | 1471 |
| 0B58 | 5A20 | 04B48 | KH | 1472 |
| 0B59 | 0D0A | 05A20 | Z | 1473 |
| 0B5A | 0000 | 0D0A | CR/LF | 1474 |
| | | 0 | NULL | 1475 |
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| 0B5B | 0004 | WJIF4 | DC | 1478 |
| 0B5C | 3820 | 4 | COUNT/8 KHZ | 1479 |
| 0B5D | 4B48 | 03820 | 8 | 1480 |
| 0B5E | 5A20 | 04B48 | KH | 1481 |
| 0B5F | 0D0A | 05A20 | Z | 1482 |
| 0B60 | 0000 | 0D0A | CR/LF | 1483 |
| | | 0 | NULL | 1484 |
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ATAC

| LOC | OBJECT CODE | CARD IMAGE | CARDNUM | PAGE |
|------|-------------|------------|---------|------|
| 0B62 | 0B6D | DC | 1485 | |
| 0B63 | 0B71 | DC | 1486 | |
| 0B64 | 0B79 | DC | 1487 | |
| 0B65 | 0B82 | DC | 1488 | |
| 0B66 | 0B87 | DC | 1489 | |
| 0B67 | 0B8C | DC | 1490 | |
| 0B68 | 0B91 | LC | 1491 | |
| | | | 1492 | |
| | | | 1493 | |
| 0B69 | 0002 | DC | 1494 | |
| 0B6A | 414D | DC | 1495 | |
| 0B6B | 0D0A | DC | 1496 | |
| 0B6C | 0000 | DC | 1497 | |
| | | | 1498 | |
| | | | 1499 | |
| 0B6D | 0002 | DC | 1500 | |
| 0B6E | 464D | DC | 1501 | |
| 0B6F | 0D0A | DC | 1502 | |
| 0B70 | 0000 | DC | 1503 | |
| | | | 1504 | |
| | | | 1505 | |
| 0B71 | 0006 | DC | 1506 | |
| 0B72 | 4246 | DC | 1507 | |
| 0B73 | 4F20 | DC | 1508 | |
| 0B74 | 4649 | DC | 1509 | |
| 0B75 | 5845 | DC | 1510 | |
| 0B76 | 4420 | LC | 1511 | |
| 0B77 | 0D0A | DC | 1512 | |
| 0B78 | 0000 | LC | 1513 | |
| | | | 1514 | |
| | | | 1515 | |
| 0B79 | 0007 | DC | 1516 | |
| 0B7A | 4246 | LC | 1517 | |
| 0B7B | 4F20 | DC | 1518 | |
| 0B7C | 5641 | DC | 1519 | |
| 0B7D | 5249 | DC | 1520 | |
| 0B7E | 4142 | DC | 1521 | |
| 0B7F | 4C45 | LC | 1522 | |
| 0B80 | 0D0A | DC | 1523 | |
| 0B81 | 0000 | DC | 1524 | |
| | | | 1525 | |
| | | | 1526 | |
| 0B82 | 0003 | DC | 1527 | |
| 0B83 | 4953 | DC | 1528 | |
| 0B84 | 4220 | DC | 1529 | |
| 0B85 | 0D0A | DC | 1530 | |
| 0B86 | 0000 | DC | 1531 | |
| | | | 1532 | |
| | | | 1533 | |
| 0B87 | 0003 | DC | 1534 | |
| 0B88 | 4C53 | LC | 1535 | |
| 0B89 | 4220 | DC | 1536 | |
| 0B8A | 0D0A | DC | 1537 | |

| ATAC | LOC | OBJECT CODE | CARD IMAGE | CARDNUM |
|------|------|-------------|------------|--|
| OB8B | 0000 | | DC 0 | 1538 1539 1540 1541 1542 1543 1544 1545 1546 1547 1548 1549 1550 1551 1552 1553 1554 1555 1556 1557 1558 1559 1560 1561 1562 1563 1564 1565 1566 1567 1568 1569 1570 1571 1572 1573 1574 1575 1576 1577 1578 1579 1580 1581 1582 1583 1584 1585 1586 1587 1588 1589 1590 |
| OB8C | 0003 | | DC | NULL |
| OB8D | 5553 | WJDM7 | DC | COUNT/USB |
| OB8E | 4220 | | DC | US |
| OB8F | 0D0A | | DC | B |
| OB90 | 0000 | | DC | CR/LF |
| OB91 | 0004 | | DC | NULL |
| OB92 | 414D | WJDM8 | DC | COUNT/AM-NL |
| OB93 | 2D4E | | DC | AM |
| OB94 | 4C20 | | DC | -N |
| OB95 | 0D0A | | DC | L |
| OB96 | 0000 | | DC | CR/LF |
| OB97 | 0003 | | DC | NULL |
| OB98 | 5343 | WJSCAN | DC | COUNT |
| OB99 | 414E | | DC | SC |
| OB9A | 0D0A | | DC | AM |
| OB9B | 0000 | | DC | CR/LF |
| OB9C | 0009 | | DC | NULL |
| OB9D | 5354 | | DC | COUNT |
| OB9E | 4152 | | DC | ST |
| OB9F | 5420 | | DC | AR |
| OB9A | 4652 | | DC | T |
| OB9A | 4551 | | DC | FR |
| OB9A | 2049 | | DC | EQ |
| OB9A | 4E20 | | DC | I |
| OB9A | 485A | | DC | N |
| OB9A | 0D0A | | DC | HZ |
| OB9A | 0000 | | DC | CR/LF |
| OB9A | 0000 | | DC | NULL |
| OB9A | 0008 | | DC | COUNT |
| OB9A | 454E | | DC | EN |
| OB9A | 4420 | | DC | D |
| OB9A | 4652 | | DC | FR |
| OB9A | 4551 | | DC | EQ |
| OB9A | 2049 | | DC | I |
| OB9A | 4E20 | | DC | N |
| OB9A | 485A | | DC | HZ |
| OB9A | 0D0A | | DC | CR/LF |
| OB9A | 0000 | | DC | NULL |
| OB9A | 0008 | | DC | COUNT |
| OB9A | 454E | | DC | EN |
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| OB9A | 485A | | DC | HZ |
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| OB9A | 0000 | | DC | NULL |
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| OB9A | 4652 | | DC | FR |
| OB9A | 4551 | | DC | EQ |

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05045
05246
04143
04520
0414E
04420
05245
04345
04956
04552
00707
00D0A
0000

CARD IMAGE

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ATAC

LOC OBJECT CODE

0BE2 494E
0BE3 5445
0BE4 5246
0BE5 4143
0BE6 4520
0BE7 414E
0BE8 4420
0BE9 5245
0BEA 4345
0BEB 4956
0BEC 4552
0BED C707
0BEE 0D0A
0BEF 0000

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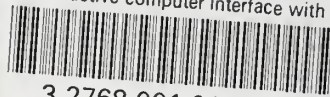
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